

1. Record Nr.	UNINA9910457069803321
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Titolo	Embedded systems architecture [[electronic resource]] : a comprehensive guide for engineers and programmers / / by Tammy Noergaard
Pubbl/distr/stampa	Amsterdam ; ; Boston, : Elsevier/Newnes, c2005
ISBN	1-281-00682-3 9786611006822 0-08-049124-3
Descrizione fisica	1 online resource (657 p.)
Collana	Embedded technology series
Disciplina	004.2/2 22 004.22
Soggetti	Computer architecture Embedded computer systems Electronic books.
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Cover; Embedded Systems Architecture; Copyright Page; Contents; Foreword; Acknowledgments; About the Author; Section I: Introduction to Embedded Systems; Chapter 1. A Systems Engineering Approach to Embedded Systems Design; 1.1 What Is an Embedded System?; 1.2 Embedded Systems Design; 1.3 An Introduction to Embedded Systems Architecture; 1.4 Why Is the Architecture of an Embedded System Important?; 1.5 The Embedded Systems Model; 1.6 Summary; Chapter 1 Problems; Chapter 2. Know Your Standards; 2.1 An Overview of Programming Languages and Examples of Their Standards 2.2 Standards and Networking2.3 Multiple Standards-Based Device Example: Digital Television (DTV); 2.4 Summary; Chapter 2 Problems; Section II: Embedded Hardware; Chapter 3. Embedded Hardware Building Blocks and the Embedded Board; 3.1 Lesson One on Hardware: Learn to Read a Schematic!; 3.2 The Embedded Board and the von Neumann Model; 3.3 Powering the Hardware; 3.4 Basic Hardware Materials: Conductors, Insulators, and Semiconductors; 3.5 Common Passive Components on Boards and in Chips: Resistors, Capacitors, and

Inductors

3.6 Semiconductors and the Active Building Blocks of Processors and Memory
3.7 Putting It All Together: The Integrated Circuit (IC); 3.8 Summary; Chapter 3 Problems; Chapter 4. Embedded Processors; 4.1 ISA Architecture Models; 4.2 Internal Processor Design; 4.3 Processor Performance; 4.4 Reading a Processor's Datasheet; 4.5 Summary; Chapter 4 Problems; Chapter 5. Board Memory; 5.1 Read-Only Memory (ROM); 5.2 Random-Access Memory (RAM); 5.3 Auxiliary Memory; 5.4 Memory Management of External Memory; 5.5 Board Memory and Performance; 5.6 Summary; Chapter 5 Problems
Chapter 6. Board I/O (Input/Output) 6.1 Managing Data: Serial vs. Parallel I/O; 6.2 Interfacing the I/O Components; 6.3 I/O and Performance; 6.4 Summary; Chapter 6 Problems; Chapter 7. Board Buses; 7.1 Bus Arbitration and Timing; 7.2 Integrating the Bus with Other Board Components; 7.3 Bus Performance; 7.4 Summary; Chapter 7 Problems; Section III: Embedded Software Introduction; Chapter 8. Device Drivers; 8.1 Example 1: Device Drivers for Interrupt-Handling; 8.2 Example 2: Memory Device Drivers; 8.3 Example 3: On-board Bus Device Drivers; 8.4 Board I/O Driver Examples; 8.5 Summary
Chapter 8 Problems Chapter 9. Embedded Operating Systems; 9.1 What Is a Process?; 9.2 Multitasking and Process Management; 9.3 Memory Management; 9.4 I/O and File System Management; 9.5 OS Standards Example: POSIX (Portable Operating System Interface); 9.6 OS Performance Guidelines; 9.7 Oses and Board Support Packages (BSPs); 9.8 Summary; Chapter 9 Problems; Chapter 10. Middleware and Application Software; 10.1 What Is Middleware?; 10.2 What Is an Application?; 10.3 Middleware Examples; 10.4 Application Layer Software Examples; 10.5 Summary; Chapter 10 Problems
Section IV: Putting It All Together: Design and Development

Sommario/riassunto

This comprehensive textbook provides a broad and in-depth overview of embedded systems architecture for engineering students and embedded systems professionals. The book is well suited for undergraduate embedded systems courses in electronics/electrical engineering and engineering technology (EET) departments in universities and colleges, as well as for corporate training of employees. The book is a readable and practical guide covering embedded hardware, firmware, and applications. It clarifies all concepts with references to current embedded technology as it exists in the industry today.

2. Record Nr.	UNINA9910964672003321
Autore	Alizade Alcira Mariam
Titolo	Psychoanalysis and positivity // Mariam Alizade
Pubbl/distr/stampa	London, : Karnac, 2010
ISBN	0-429-91775-9 0-429-90352-9 0-429-47875-5 1-282-77987-7 9786612779879 1-84940-711-8
Edizione	[First edition.]
Descrizione fisica	1 online resource (219 p.)
Disciplina	150.19/88 150.1988
Soggetti	Positive psychology Psychoanalysis Emotions
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Cover; Copyright; Contents; A Few Words About The English Version; Introductory Remarks; Chapter One: Positivity in psychoanalysis; Chapter Two: Theoretical fundamentals; Chapter Three: Clinical work and positivity; Chapter Four: Psychoanalysis and mental health; Chapter Five: Repetition and its reversal; Chapter Six: Trauma and positivity; Chapter Seven: The internal setting; Chapter Eight: Reanalysis and impasse; Chapter Nine: Clinical vignettes; Chapter Ten: An essay on joy; Closing Words; References
Sommario/riassunto	Without falling into unwarranted enthusiasm or naive optimism, the book examines how positivity operates, and goes on to investigate the concept of the construction of an internal framework, the reversal of repetition, and the problematic issues raised by impasse and trauma. Just as psychoanalytic treatment without tears does not exist the book argues that neither does psychoanalytic treatment without joyfulness. Tears and laughter are part of the universe of the analysts consulting

roomand in the clinical fluctuation between distress and satisfaction, pleasure and displeasure, the analyst accepts both extremes. Humour becomes therapeutic, as do outbreaks of joyfulness in sessions, when the mind is fleetingly freed from the burden of illness.
