Record Nr. UNINA9910962956703321 Autore Hsiung Pao-Ann Titolo Multicore hardware-software design and verification techniques / / edited by Pao-Ann Hsiung, Yean-Ru Chen, Chao-Sheng Lin [Oak Park, III.?], : Bentham eBooks, [2011] Pubbl/distr/stampa 9781608052257 **ISBN** 1608052257 Edizione [1st ed.] Descrizione fisica 1 online resource (105 p.) HsiungPao-Ann Altri autori (Persone) ChenYean-Ru LinChao-Sheng Disciplina 005.10685 Soggetti Computer software - Development Software engineering Computer input-output equipment Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Description based upon print version of record. Note generali Nota di bibliografia Includes bibliographical references and index. Nota di contenuto 01 Title.pdf; 02 Cover Page; 03 eBooks End User License Agreement-Website; 04 Dedication; 05 Contents; 06 Foreword; 07 Preface; 08 Listof-Contributors; 09 ACKNOWLEDGMENTS; 10 Chapter 01; 11 Chapter 02; 12 Chapter 03; 13 Chapter 04; 14 Chapter 05; 15 Index Sommario/riassunto The surge of multicore processors coming into the market and on users' desktops has made parallel computing the focus of attention once again. This time, however, it is led by the industry, which ensures that multicore computing is here to stay. Nevertheless, there is still so much research work to be done in multicore hardware-software designs before consumer applications can leverage the benefits of this

new paradigm. This e-book is being put forward as a platform for immediate collection of state-of-the-art technologies in both hardware

and software designs for multicore computing. With the