1. Record Nr. UNINA9910916801403321

Autore	Prešl, Karl Borsiwoj <1794-1852)>
Titolo	Prodromus Florae Siculae di Presl
Pubbl/distr/stampa	Pragae, 1819
Descrizione fisica	64 p. ; 8°
Disciplina	581.9458
Locazione	DBV
Collocazione	D III 27 (1
Lingua di pubblicazione	Latino
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	A rediscovered work of Karel B. Presl and other sins of his youth. Taxon 69: 1293-1310

2. Record Nr. UNINA9910962728103321

Autore	Roberts Gordon W. <1959->
Titolo	Design and analysis of integrator-based log-domain filter circuits / / Gordon W. Roberts and Vincent W. Leung
Pubbl/distr/stampa	Boston, : Kluwer Academic, c2000
ISBN	1-280-20604-7 9786610206049 0-306-47054-3
Edizione	[1st ed. 2002.]
Descrizione fisica	1 online resource (279 p.)
Collana	The Springer International Series in Engineering and Computer Science, , 0893-3405 ; ; 534
Altri autori (Persone)	LeungVincent W
Disciplina	621.3815/324
Soggetti	Log domain filters - Design and construction Electric circuit analysis Metal oxide semiconductors, Complementary - Design and construction Bipolar integrated circuits - Design and construction
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia

Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Log-Domain Integrators -- Log-Domain Filter Synthesis-I: Operational Simulation of LC Ladders -- Log-Domain Filter Synthesis-II: State-Space Formulation -- Nonideality Analysis of Biquadratic Log-Domain Filters -- Extending the Nonideality Analysis to High-Order Ladder Filters -- Experimental 1C Prototypes.
Sommario/riassunto	Design and Analysis of Integrator-Based Log-Domain Filter Circuits deals with the design and analysis of log-domain filter circuits. It describes several synthesis methods that aid the designer in developing bipolar or BiCMOS filter circuits with cut-off frequencies ranging from the low kilohertz range to several hundreds of megahertz. Filter response deviations due to transistor-level nonidealities are systematically analyzed, leading to effective electronic compensation schemes. Numerous examples are provided in the text with measured experimental data from IC prototypes. Design and Analysis of Integrator-Based Log-Domain Filter Circuits is intended for engineers in research or development, as well as advanced-level engineering students. Extensive discussion on filter text metrics should also interest engineers who are responsible for testing high-performance, high-speed analog or mixed-signal products.