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Sommario/riassunto	This book provides a complete overview of high-speed circuit design for high-speed telecommunication above 100GHz. Covering everything from telecom and electronics fundamentals to system-level modeling, detailed circuit design, and in-depth performance analysis, this book lends itself as the perfect design guide and reference work for beginner and experienced telecommunication circuit designers alike. Provides extensive coverage of the basics of telecommunication & fundamentals of electronic circuit design; Describes practical methodology and design guidelines for broadband CMOS circuits above 100GHz; Uses insightful system-level modeling with a link to practical system and

circuit design.