1. Record Nr. UNINA9910877807203321

Titolo	Digital system clocking : high performance and low-power aspects / / Vojin G. Oklobdzija ... [et al.]
Pubbl/distr/stampa	New York, : IEEE Hoboken, N.J., : Wiley-Interscience, c2003
ISBN	1-280-25290-1 9786610252909 0-470-34300-1 0-471-72368-1 0-471-72370-3
Edizione	[1st edition]
Descrizione fisica	1 online resource (265 p.)
Altri autori (Persone)	OklobdzijaVojin G
Disciplina	621.3815
Soggetti	Timing circuits - Design and construction Memory management (Computer science) Low voltage integrated circuits - Design and construction High performance computing Electronic digital computers - Power supply Electric power - Conservation
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references (p. 233-240) and index.
Nota di contenuto	Preface. -- Introduction. -- Theory of Clocked Storage Elements. -- Timing and Energy Parameters. -- Pipelining and Timing Analysis. -- High-Performance System Issues. -- Low-Energy System Issues. -- Simulation Techniques. -- State-of-the-Art Clocked Storage Elements in CMOS Technology. -- Microprocesor Examples. -- References. -- Index.
Sommario/riassunto	Provides the only up-to-date source on the most recent advances in this often complex and fascinating topic. . The only book to be entirely devoted to clocking. Clocking has become one of the most important topics in the field of digital system design. A "must have" book for advanced circuit engineers.