

1. Record Nr.	UNINA9910872846203321
Titolo	Advanced Research in Asynchronous Circuits and Systems: Proceedings 6th International Symposium, Eilat, Israel, 2000
Pubbl/distr/stampa	[Place of publication not identified], : IEEE Computer Society Press, 2000
Descrizione fisica	1 online resource (xi, 221 pages) : illustrations
Disciplina	004.66
Soggetti	Asynchronous transfer mode
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Sommario/riassunto	<p>Annotation Contains papers from an April 2000 symposium, coming from a mixture of industrial and academic origins. Papers are arranged in sections on theory and verification techniques, asynchronous design in embedded systems, testability, synthesis, arbitration and circuit techniques, processor design, and pushing the performance limit. Specific topics include composing snippets, applying asynchronous circuits in contactless smart cards, high-level asynchronous system design using the ACK framework, priority arbiters, an instruction buffer for a low- power DSP, and low-latency asynchronous FIFOs using token rings. Lacks a subject index. Annotation copyrighted by Book News, Inc., Portland, OR.</p>