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Descrizione fisica	1 online resource (xi, 221 pages) : illustrations
Disciplina	004.66
Soggetti	Asynchronous transfer mode
Lingua di pubblicazione	Inglese
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Sommario/riassunto	Annotation Contains papers from an April 2000 symposium, coming from a mixture of industrial and academic origins. Papers are arranged in sections on theory and verification techniques, asynchronous design in embedded systems, testability, synthesis, arbitration and circuit techniques, processor design, and pushing the performance limit. Specific topics include composing snippets, applying asynchronous circuits in contactless smart cards, high-level asynchronous system design using the ACK framework, priority arbiters, an instruction buffer for a low- power DSP, and low-latency asynchronous FIFOs using token rings. Lacks a subject index. Annotation copyrighted by Book News, Inc., Portland, OR.