

1. Record Nr.	UNINA9910872833003321
Titolo	Innovative architecture for future generation high-performance processors and systems : 18-19 January, 2001, Maui, Hawaii
Pubbl/distr/stampa	[Place of publication not identified], : IEEE Computer Society, 2000
Soggetti	Computer architecture High performance computing High performance processors Engineering & Applied Sciences Computer Science
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph