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Sommario/riassunto	Proceedings of the symposium held in Cancun, Mexico, April 1994. Twenty-one technical sessions cover interconnection networks, languages, computational geometry, memory issues, compiling, graph algorithms, machine architectures, compiler optimization, algorithms, sorting and networks, resource management, numerical methods, routing, synchronization, performance, embedding and message passing, models and real time systems, applications, network performance, data structures and debugging, and image processing and VLSI CAD. Three industrial track sessions cover parallel processing systems, digital media and distributed computing, and portability, scalability, and applications.