

1. Record Nr.	UNINA9910872644703321
Autore	Burgess Neil
Titolo	15th Symposium on Computer Arithmetic, 2001, Vail, CO
Pubbl/distr/stampa	[Place of publication not identified], : IEEE Computer Society Press, 2001
Descrizione fisica	1 online resource (285 pages)
Disciplina	004.0151
Soggetti	Computer arithmetic
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di contenuto	Proceedings 15th IEEE Symposium on Computer Arithmetic. ARITH-15 2001 -- Leading zero anticipation and detection-a comparison of methods -- Bounds on runs of zeros and ones for algebraic functions -- Binary multiplication radix-32 and radix-256 -- Analysis of column compression multipliers -- Faithful powering computation using table look-up and a fused accumulation tree -- Bit-parallel systolic modular multipliers for a class of GF(2 ^m) -- Modular multiplication and base extensions in residue number systems -- Efficient computation of multiplicative inverses for cryptographic applications -- Optimised squaring of long integers using precomputed partial products.
Sommario/riassunto	The proceedings from the June 2001 conference in Vail, Colorado, feature 30 papers on binary strings, multiplication and exponentiation, cryptography, division and square root, elementary functions and rounding, number systems, floating high points, addition, logarithmic number systems, and on-line arithmetic. An abstract of the keynote speech offers a processor architect's perspective on computer arithmetic. And a reprint of Knowles's "A Family of Adders," which was mis-printed in the proceedings from the previous conference, is included. Contributors represent 11 countries. Name index only. c. Book News Inc.