

1. Record Nr.	UNIPARTHENOPE000029859
Titolo	Diciannovesimo rapporto sulle migrazioni 2013 / Fondazione ISMU
Pubbl/distr/stampa	Milano : Franco Angeli, c2014
Titolo uniforme	Diciannovesimo rapporto sulle migrazioni 2013
ISBN	978-88-204-6888-0
Descrizione fisica	248 p. ; 23 cm
Collana	Collana Ismu ; 55
Disciplina	304.8
Collocazione	304-D/4
Lingua di pubblicazione	Italiano
Formato	Materiale a stampa
Livello bibliografico	Monografia
2. Record Nr.	UNINA9910495681003321
Autore	Achard Pierre
Titolo	Le rôle des ministères des Finances et de l'Economie dans la construction européenne (1957-1978) : Tome I // René Girault, Raymond Poidevin
Pubbl/distr/stampa	Vincennes, : Institut de la gestion publique et du développement économique, 2020
ISBN	2-11-129445-4
Descrizione fisica	1 online resource (572 p.)
Collana	Histoire économique et financière - XIXe-XXe
Altri autori (Persone)	BadelLaurence BossuatGérard BührerWemer BussièreÉric ChickMartin de BoissieuChristian de CastelnauAnne de LattreAndré DumoulinMichel FeiertagOlivier FrankRobert GiraultRené HarryvanAnjo G HazouméBoris JacquemartClaude KippingMatthias LefèvreSylvie

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Willem BrouwerJan

Disciplina

339.5/094/09045

Soggetti

Monetary policy - European Economic Community countries
Fiscal policy - European Economic Community countries
Finance ministers - European Economic Community countries
Europe Economic integration Congresses

Lingua di pubblicazione

Francese

Formato

Materiale a stampa

Livello bibliografico

Monografia

Nota di contenuto

t. 1. Actes du colloque tenu a Bercy les 26, 27, 28 mai 1999 -- t. 2. Journées préparatoires tenues a Bercy le 14 novembre 1997 et le 29 janvier 1998.

Sommario/riassunto

Quel rôle ont joué les ministères des Finances et de l'Économie dans la construction européenne ? Comment ont réagi les différentes administrations, les hauts fonctionnaires face aux nouveautés structurelles, monétaires et fiscales ? Quelles solutions a-t-on trouvé hors de l'Hexagone ? Les trente intervenants réunis lors de ce colloque apportent leurs réponses à ces questions. Leurs communications sont regroupées autour de quatre thèmes : politiques économiques et concertations dans les années soixante et soixante-dix ; administration et culture européenne ; naissance d'une politique monétaire européenne ; négociations de la PAC et du GATT. Ces Actes sont complétés par la publication du témoignage d'acteurs de la construction européenne autour de l'union monétaire, des plans Barre et Werner et de l'harmonisation des taxes sur la valeur ajoutée dans les années soixante et soixante-dix.

3. Record Nr.	UNINA9910861097303321
Titolo	Advances in Microelectronics, Embedded Systems and IoT : Proceedings of 8th International Conference on Microelectronics, Electromagnetics and Telecommunications (ICMEET 2023) // edited by V. V. S. S Chakravarthy, Vikrant Bhateja, Jaume Anguera, Shabana Urooj, Anumoy Ghosh
Pubbl/distr/stampa	Singapore : , : Springer Nature Singapore : , : Imprint : Springer, , 2024
ISBN	981-9707-67-6
Edizione	[1st ed. 2024.]
Descrizione fisica	1 online resource (0 pages)
Collana	Lecture Notes in Electrical Engineering, , 1876-1119 ; ; 1156
Disciplina	537
Soggetti	Electronic circuits Electronics Embedded computer systems Internet of things Electronic Circuits and Systems Electronics and Microelectronics, Instrumentation Embedded Systems Internet of Things
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Intro -- Committee -- Preface -- Contents -- Editors and Contributors -- Hardware Security for IC Piracy: Logic Locking Past, Present and Opportunity -- 1 Introduction -- 2 Background -- 2.1 Basics of Logic Locking -- 3 Logic Locking: Defence Techniques -- 3.1 Primitive Logic Locking -- 3.2 Cyclic-Based-Logic Locking -- 3.3 Scan Chain Logic Locking/Blocking -- 3.4 Finite State Machine-Based Logic Locking -- 4 Logic Locking: Attacks -- 4.1 Algorithmic Attacks -- 4.2 Approximate Attacks -- 4.3 Structural Attacks -- 5 Research Opportunities in Logic Locking -- 6 Conclusion -- References -- Design and Analysis of 2.4 to 3.5 GHz Low-Noise Amplifier for Sub-6 Cellular LTE/5G NR Application Using CMOS 110 nm SOI Process Technology -- 1 Introduction -- 2 Selection of Process Technology (CMOS SOI) -- 3 Topology Selection for Proposed LNA -- 4 Transistor

Size Selection for Proposed LNA Design -- 5 Impedance Matching for Proposed LNA Design -- 6 Circuit Design of proposed LNA Design -- 7 Performance of Designed LNA Abbreviations and Acronyms -- 7.1 S Parameter -- 7.2 Noise Figure -- 7.3 Stability -- 7.4 Linearity -- 8 Comparison of Proposed LNA Design -- 9 Conclusion -- References --

Out-of-Order Execution of Instructions for In-Order Five-Stage RISC-V Processor -- 1 Introduction -- 2 System Design -- 3 Results -- 4 Conclusion -- References --

Load Balancing by Efficient Resource Allocation for Edge Devices -- 1 Introduction -- 2 Proposed Method -- 2.1 The First Stage Is to Build the Simulator that Mainly Contains 3 Physical Entities -- 2.2 Finding the Most Appropriate Load Balancing Algorithm and Adding It to the Simulator -- 3 Evaluating the Performance of the Simulator -- 4 Conclusion -- References --

Analysis of Clock Gating Techniques for Low Power -- 1 Introduction -- 2 Power Analysis of Existing Clock Gating Techniques -- 2.1 AND-Based Clock Gating. 2.2 Flip-Flop-Based Clock Gating -- 2.3 Latch-Based Clock Gating (LBCG) -- 2.4 Data-Driven-Based Clock Gating -- 2.5 Autogated-Based Clock Gating (ABCG) -- 3 Experimental Results -- 4 Conclusion -- References --

Design and Development of Physiological Parameter Monitoring System Using Wearable Sensors -- 1 Introduction -- 1.1 Background Study -- 2 Research Methods -- 2.1 Material and Study -- 2.2 Block Diagram -- 2.3 Flowchart -- 2.4 Circuit Diagram -- 3 Testing -- 3.1 Data Retrieval -- 4 Conclusion -- References --

Energy-Efficient Enhancement of the AES Module in Randshift Method Through S-Box with Registers -- 1 Introduction -- 2 Literature Survey -- 3 Methodology -- 3.1 Existing AES in Randshift -- 3.2 Proposed Methodology -- 4 Simulation Results -- 5 Conclusion and Future Scope -- References --

Numerical Exploration of Supercontinuum Generation in Zinc-Germanium Diphosphide-Based Photonic Crystal Fiber -- 1 Introduction -- 2 Proposed Design of PCF -- 3 Linear and Nonlinear Parameters -- 4 Optical Properties of the Fiber -- 5 Numerical Simulation Outcomes for SCG -- 6 Conclusion and Future Scope -- References --

Adaptive Learning-Based IoT Security Framework Using Recurrent Neural Networks -- 1 Introduction -- 1.1 Background and Motivation -- 2 Literature Review -- 3 Methodology -- 3.1 Overview of the Proposed Adaptive Learning-Based IoT Security Framework -- 3.2 Sensor Parameters for IoT Security Analysis -- 3.3 Data Collection and Preprocessing for RNN Training -- 3.4 Model Evaluation and Validation Techniques -- 4 Implementation -- 4.1 Selection of IoT Devices and Network Setup -- 4.2 Integration of the RNN Model into the IoT Security Framework -- 5 Results and Analysis -- 5.1 Performance Evaluation Metrics -- 5.2 Comparison of the Proposed Framework with Traditional Methods -- 6 Conclusion -- References.

Design of Radial Basis Function with PI-Based Supervisory Neural Controller for Liquid Level System -- 1 Introduction -- 2 RBF-PI-Based Supervisory Controller -- 3 Discrete-Time PI Controller with Anti-windup (AW) Strategy -- 4 Experimental Results -- 5 Conclusion -- References --

A Novel Approach for the Design of a Raspberry Pi-Based Smart Drainage Monitoring System -- 1 Introduction -- 2 Methodologies and Hardware Tools Used -- 2.1 SMTP Protocol -- 2.2 Hardware Tools -- 3 Proposed System Design -- 3.1 Working Methodology of the Proposed System -- 4 Programming Methodology -- 5 Results and Observation -- 6 Conclusion and Future Work -- References --

Design of an IoT-Based System for Prefailure Deformation Monitoring of Riverbank Landslides in Mekong Delta -- 1 Introduction -- 2 System Design -- 2.1 Structure of the Sensor Nodes

-- 2.2 Cloud Server -- 3 Experimental Results -- 4 Conclusions --
References -- Software-Defined Storage Performance Testing Using
Mininet -- 1 Introduction -- 2 Related Work -- 3 Emulation Scenario --
4 Emulation Setting -- 4.1 Emulation Environment Specifications -- 5
Emulation Results -- 6 Conclusion and Future Scope -- 6.1 Conclusion
-- 6.2 Future Scope -- References -- High Gain Voltage Lift-Based
Interleaved Cascaded Boost DC-DC Converter -- 1 Introduction -- 2
Circuit Analysis of the Proposed Converter -- 3 Operating Principle
of Proposed Converter -- 4 Proposed Converter Steady-State Analysis
-- 4.1 Voltage Gain (G) -- 4.2 Ratings of Diodes -- 4.3 Ratings
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Fuel Cell Electric Vehicles -- 1 Introduction -- 2 Modelling of Fuel Cell
-- 3 Modelling of DC-DC Converters -- 3.1 Interleaved Boost Converter
(IBC) -- 3.2 Self-lift SEPIC Converter -- 4 Inverter, Motor and Motor
Control Design.
4.1 Modelling of Inverter -- 4.2 BLDC Motor and Control -- 5
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Assessment of All-in-One Portable Measurement Device for Home-
Based Remote Vital Sign Monitoring -- 1 Introduction -- 2 Material
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Calibration -- 2.3 Part 2 Mild Symptom Patient Self-assessment -- 3
Results and Discussion -- 3.1 Part 1 PVS Calibration -- 3.2 Part 2 Mild
Symptom Patient Self-assessment -- 3.3 Discussion -- 4 Conclusion --
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with Reduced Graphene Oxide as an HTL: A Simulation Analysis Using
SCAPS-1D -- 1 Introduction -- 2 Modelling of Perovskite Solar Cell --
3 Simulation of the Device -- 4 Results and Discussion -- 4.1 Effect
of Perovskite Layer Thickness -- 4.2 Effect of Perovskite Layer Doping
-- 4.3 Effect of TCO Layer Thickness -- 4.4 Effect of Reduced Graphene
Oxide (rGO) Doping -- 5 Conclusion -- References -- Design
and Analysis of 10-nm FD-SOI FinFET by Dual-Dielectric Spacers
for High-Speed Switching -- 1 Introduction -- 2 Structure
and Simulation Framework of the Device -- 2.1 Single Dielectric
Spacers and Simulation Results -- 2.2 Dual-k Spacers with Device
Structure and Simulation Results -- 2.3 Doping Concentration -- 3
Simulation Results of Single- and Dual-Dielectric Spacer Devices -- 4
Conclusion -- References -- Study of Effect of Nd Substitution
on Dielectric and Electrical Properties of Bismuth Iron Titanate -- 1
Introduction -- 2 Experimental Procedure -- 3 Analysis of Experimental
Results -- 3.1 XRD and Morphological Analysis -- 3.2 Dielectric
Analysis -- 3.3 Impedance Analysis -- 3.4 AC Conductivity -- 4
Conclusion -- References -- Initiation, Innovation, Implementation
and Integration of CBDC in Digital Virtual Payment Systems -- 1
Introduction.
2 Background Information -- 2.1 Blockchain -- 2.2 Different Forms
of Digital Currency -- 2.3 Difference Between Regular Online Payments
and CBDC -- 3 Proposed 4i Stage Model of CBDC -- 3.1 Initiation
of CBDC -- 3.2 Innovations of CBDC -- 3.3 Implementation of CBDC --
4 Integration of CBDC -- 5 Conclusion -- References -- Software
Development Effort Estimation Using UML Activity Models
with Regression Analysis -- 1 Introduction -- 2 Relevant Work -- 3
Proposed Approach -- 3.1 Dataset -- 3.2 Regression Models -- 3.3
Performance Metrics -- 4 Environment Setup -- 5 Results Analysis -- 6
Conclusion -- References -- An IoT-Based Intelligent Smart Parking
System with Effective Communication System -- 1 Introduction -- 2
Literature Review -- 3 Methodology -- 3.1 Design Methodologies --
3.2 Connections -- 3.3 System Architecture Diagram -- 3.4 Hardware

Requirements -- 3.5 Software Requirements -- 4 Results and Findings -- 4.1 Stepwise Description of Implementation -- 4.2 Results -- 4.3 Observations from the Work -- 5 Conclusion and Future Scope -- 5.1 Conclusion -- 5.2 Future Study -- References -- Smart Pregnancy Watch with Location-Based Emergency Messaging, a Comprehensive Solution for Maternal Health Care -- 1 About Smart Band -- 2 Survey for Pregnancy in Women -- 3 Problems Identified -- 3.1 Discussion About the Problem -- 3.2 Emergency Message Sending -- 3.3 Location Tracking -- 3.4 Baby Safety -- 4 Hardware Components -- 5 Design and Implementation -- 6 Results and Discussion -- 7 Conclusion -- References -- ANN Enabled Obstacle Avoiding Automated Car -- 1 Introduction -- 2 Scope -- 3 Overview -- 4 Related Work -- 5 Existing System -- 6 Proposed System -- 7 Problem Statement -- 8 Methodology -- 8.1 Circuit Diagram -- 8.2 Algorithm -- 8.3 Data Flow Diagram -- 8.4 Pseudo-code -- 9 Overview of Technology -- 9.1 Arduino UNO -- 9.2 Ultrasonic Sensor. 9.3 TT Gear Set and Wheels.

Sommario/riassunto

The book discusses the latest developments and outlines future trends in the fields of microelectronics, electromagnetics, and telecommunication. It contains original research works presented at the International Conference on Microelectronics, Electromagnetics and Telecommunication (ICMEET 2023), organized by Department of Electronics and Communication Engineering, National Institute of Technology Mizoram, India, during 6–7 October 2023. The book is divided into two volumes, and it covers papers written by scientists, research scholars, and practitioners from leading universities, engineering colleges, and R&D institutes from all over the world and shares the latest breakthroughs in and promising solutions to the most important issues facing today's society.
