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| ISBN | 1-283-33753-3 9786613337535 1-118-17115-2 |
| Descrizione fisica | 1 online resource (314 p.) |
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| Soggetti | Frequency synthesizers Phase-locked loops |
| Lingua di pubblicazione | Inglese |
| Formato | Materiale a stampa |
| Livello bibliografico | Monografia |
| Note generali | Description based upon print version of record. |
| Nota di bibliografia | Includes bibliographical references and index. |
| Nota di contenuto | Frontmatter -- Introduction -- Fractional-N and Basic S? Synthesizers -- Other Spurious Reduction Techniques -- Defects in S? Synthesizers -- Other S? Architectures -- Simulation -- Diophantine Synthesizer -- Operation at Extreme Bandwidths -- All-Digital Frequency Synthesizers -- Appendix A: All Digital -- Appendix C: Fractional Cancellation -- Appendix E: Excess PPSD -- Appendix F: References to -- Appendix G: Using Gsmpl -- Appendix H: Sample-and-Hold Circuit -- Appendix L: Loop Response -- Appendix M: Mash PPSD -- Appendix N: Sampled Noise -- Appendix O: Oscillator Spectrums -- Appendix P: Phase Detectors -- Appendix Q: Quantization PPSD -- Appendix R: Reference Frequency Spurs -- Appendix S: Spectrum Analysis -- Appendix T: Toolboxes -- Appendix U: Noise Produced by Charge Pump Current Unbalance (Mismatch) -- Appendix W: Getting Files from the Wiley Internet Site -- Appendix X: Some Tables -- End Notes -- References -- Index. |
| Sommario/riassunto | "An addendum to the popular Frequency Synthesis by Phase Lock, 2nd ed, this book describes sigma-delta, a frequency synthesis technique that has gained prominence in recent years. In addition, Simulink will be employed extensively to guide the reader. Fractional-n, the still-used forerunner to sigma-delta, is also discussed. Sequences of |

simulated results allow the reader to gain a deeper understanding while detailed appendices provide information from various stages of development. Simulation models discussed in the chapters that are available online"--Provided by publisher.
