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3.8 Quine-McCluskey Method 3.9 Implementation of Digital Circuits with NAND, NOR, and Exclusive-OR Exclusive-NOR Gates; 3.9.1 NAND Gate Implementation; 3.9.2 NOR Gate Implementation; 3.9.3 XOR / XNOR Implementations; Questions and Problems; 4. COMBINATIONAL LOGIC DESIGN; 4.1 Basic Concepts; 4.2 Analysis of a Combinational Logic Circuit; 4.3 Design of a Combinational Circuit; 4.4 Multiple-Output Combinational Circuits; 4.5 Typical Combinational Circuits; 4.5.1 Binary / BCD Adders and Binary Subtractors; 4.5.2 Comparators; 4.5.3 Decoders; 4.5.4 Encoders; 4.5.5 Multiplexers; 4.5.6 Demultiplexers
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5.9 Design of Synchronous Sequential Circuits

Sommario/riassunto

Fundamentals of Digital Logic and Microcomputer Design, has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers. In this Fifth Edition, the author focuses on computer design at three levels: the device level, the logic level, and the system level. Basic topics are covered, such as number systems and Boolean algebra, combinational and sequential logic design, as well as more advanced subjects such as assembly language programming and microprocessor-based system design. Numerous examples
