

1. Record Nr.	UNINA9910829873703321
Autore	Mayer-Lindenberg F
Titolo	Dedicated digital processors [[electronic resource]] : methods in hardware/software system design // F. Mayer-Lindenberg
Pubbl/distr/stampa	Hoboken, N.J., : J. Wiley, c2004
ISBN	1-280-26941-3 9786610269419 0-470-09282-3 0-470-09284-X
Edizione	[1st edition]
Descrizione fisica	1 online resource (316 p.)
Disciplina	621.39/16 621.3916
Soggetti	Microprocessors - Design and construction Computer software
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references (p. [291]-293) and index.
Nota di contenuto	DEDICATED DIGITAL PROCESSORS; Contents; Preface; 1 Digital Computer Basics; 1.1 Data Encoding; 1.1.1 Encoding Numbers; 1.1.2 Code Conversions and More Codes; 1.2 Algorithms and Algorithmic Notations; 1.2.1 Functional Composition and the Data Flow; 1.2.2 Composition by Cases and the Control Flow; 1.2.3 Alternative Algorithms; 1.3 Boolean Functions; 1.3.1 Sets of Elementary Boolean Operations; 1.3.2 Gate Complexity and Simplification of Boolean Algorithms; 1.3.3 Combined and Universal Functions; 1.4 Timing, Synchronization and Memory; 1.4.1 Processing Time and Throughput of Composite Circuits 1.4.2 Serial and Parallel Processing 1.4.3 Synchronization; 1.5 Aspects of System Design; 1.5.1 Architectures for Digital Systems; 1.5.2 Application Modeling; 1.5.3 Design Metrics; 1.6 Summary; Exercises; 2 Hardware Elements; 2.1 Transistors, Gates and Flip-Flops; 2.1.1 Implementing Gates with Switches; 2.1.2 Registers and Synchronization Signals; 2.1.3 Power Consumption and Related Design Rules; 2.1.4 Pulse Generation and Interfacing; 2.2 Chip Technology; 2.2.1 Memory Bus Interface; 2.2.2 Semiconductor Memory Devices; 2.2.3 Processors

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2.3 Chip Level and Circuit Board-Level Design 2.3.1 Chip Versus Board-
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Architecture Definitions; 3.5 Timing Behavior and Simulation; 3.6 Test
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Point Add, Subtract, and Compare
4.3 Add and Subtract for Redundant Codes 4.4 Binary Multiplication; 4.5
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Interfacing Techniques; 6.5.1 Pipelining Input and Output
6.5.2 Parallel and Serial Interfaces, Counters and Timers

Sommario/riassunto

The recent evolution of digital technology has resulted in the design of digital processors with increasingly complex capabilities. The implementation of hardware/software co-design methodologies provides new opportunities for the development of low power, high speed DSPs and processor networks. Dedicated digital processors are digital processors with an application specific computational task. Dedicated Digital Processors presents an integrated and accessible approach to digital processor design principles, processes, and implementations based upon the author's considerable experienc
