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| Autore                  | Zhang Yu <1978 April 20->                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
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| Collana                 | Wiley series in microwave and optical engineering                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| Altri autori (Persone)  | SarkarTapan (Tapan K.)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
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| Note generali           | Description based upon print version of record.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| Nota di bibliografia    | Includes bibliographical references and index.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| Nota di contenuto       | In-core and out-of-core LU factorization for solving a matrix equation -- A parallel MoM code using RWG basis functions and ScaLAPACK-based in-core and out-of-core solvers -- A parallel MoM code using higher-order basis functions and ScaLAPACK-based in-core and out-of-core solvers -- Tuning the performance of a parallel integral equation solver -- Refinement of the solution using the iterative conjugate gradient method -- A parallel MoM code using higher-order basis functions and PLAPACK-based in-core and out-of-core solvers -- Applications of the parallel frequency-domain integral equation solver : |

TIDES.

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Sommario/riassunto

A step-by-step guide to parallelizing cem codes The future of computational electromagnetics is changing drastically as the new generation of computer chips evolves from single-core to multi-core. The burden now falls on software programmers to revamp existing codes and add new functionality to enable computational codes to run efficiently on this new generation of multi-core CPUs.

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