

1. Record Nr.	UNINA9910824563303321
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Titolo	ESD : circuits and devices // Steven H. Voldman
Pubbl/distr/stampa	Hoboken, NJ, : John Wiley, 2015
ISBN	9781118954485 1118954483 9781118954492 1118954491 9781118954478 1118954475 9781280339677 1280339675 9780470033470 0470033479 9780470030066 0470030062
Edizione	[Second edition.]
Descrizione fisica	1 online resource (414 p.)
Disciplina	621.381
Soggetti	Integrated circuits - Protection Electronic apparatus and appliances - Protection Static eliminators Electric discharges Electrostatics
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Electrostatic discharge -- Design synthesis -- Mosfet ESD design -- ESD design : diode design -- ESD design : passive resistors -- Passives for digital, analog, and RF applications -- Off-chip drivers and ESD -- Receiver circuits -- Silicon on insulator (SOI) ESD design -- ESD circuits : BiCMOS -- ESD power clamps -- Bipolar ESD power clamps -- Silicon-controlled rectifier power clamps.
Sommario/riassunto	The scaling of semiconductor devices from sub-micron to nanometer

dimensions is driving the need for understanding the design of electrostatic discharge (ESD) circuits, and the response of these integrated circuits (IC) to ESD phenomena. ESD Circuits and Devices provides a clear insight into the layout and design of circuitry for protection against electrical overstress (EOS) and ESD. With an emphasis on examples, this text:explains ESD buffering, ballasting, current distribution, design segmentation, feedback, coupling, and de-coupling ESD design methods;outlines the fun
