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| Descrizione fisica      | 1 online resource (914 p.)   |
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| Soggetti                | Linear integrated circuits - Design and construction<br>Metal oxide semiconductors, Complementary - Design and construction  |
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| Formato                 | Materiale a stampa   |
| Livello bibliografico   | Monografia   |
| Note generali           | Description based upon print version of record.  |
| Nota di bibliografia    | Includes bibliographical references.   |
| Nota di contenuto       | Front Cover; Contents; Preface; List of Figures; List of Tables; 1. Mixed-Signal Integrated Systems: Limitations and Challenges; 2. MOS Transistors; Bibliography; 3. Physical Design of MOS Integrated Circuits; Bibliography; 4. Bias and Current Reference Circuits; Bibliography; 5. CMOS Amplifiers; Bibliography; 6. Nonlinear Analog Components; Bibliography; 7. Continuous-Time Circuits; Bibliography; 8. Switched-Capacitor Circuits; Bibliography; 9. Data Converter Principles; Bibliography; 10. Nyquist Digital-to-Analog Converters; Bibliography; 11. Nyquist Analog-to-Digital Converters<br>Bibliography12. Delta-Sigma Data Converters; Bibliography; 13. Circuits for Clock Signal Generation and Synchronization; Bibliography; Appendix A: Logic Building Blocks; Appendix B: Transistor sizing in building blocks; Appendix C: Signal-Flow Graph |
| Sommario/riassunto      | High-speed, power-efficient analog integrated circuits can be used as standalone devices or to interface modern digital signal processors and micro-controllers in various applications, including multimedia, communication, instrumentation, and control systems. New architectures and low device geometry of complementary metaloxidesemiconductor (CMOS) technologies have accelerated the movement toward system on a chip design, which merges analog   |

circuits with digital, and radio-frequency components. CMOS: Analog  
Integrated Circuits: High-Speed and Power-Efficient Design describe

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