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; 3. Guidelines for success ; 4. Benchmarking and examples ; 5. Discussion ; 6. Conclusion ; Acknowledgments ; References

1.4 Three-Dimensional Integration of Ge and Two-Dimensional Materials for One-Dimensional Devices1. Introduction ; 2. FEOL technology and materials for 3D integration ; 3. Integration of "more than Moore" functionality ; 4. Implications of 3D integration at the system level ; 5. Conclusion ; Acknowledgments ; References

1.5 Challenges to Ultralow-Power Semiconductor Device Operation1. Introduction ; 2. Ultimate MOS transistors ; 3. Small slope switches ; 4. Conclusion ; Acknowledgments ; References ; 1.6 A Universal Nonvolatile Processing Environment; 1. Introduction ; 2. Universal nonvolatile processing environment 3. Bias-field-free spin-torque oscillator
