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| Autore                  | Blanchet Gerard   |
| Titolo                  | Computer architecture [[electronic resource] /] / Gerard Blanchet, Bertrand Dupouy  |
| Pubbl/distr/stampa      | London, : ISTE<br>Hoboken, N.J., : Wiley, 2013  |
| ISBN                    | 1-78539-348-0<br>1-118-57743-4<br>1-299-14631-7<br>1-118-57779-5<br>1-118-57778-7   |
| Edizione                | [1st ed.]   |
| Descrizione fisica      | 1 online resource (380 p.)  |
| Collana                 | Computer engineering series   |
| Altri autori (Persone)  | DupouyBertrand  |
| Disciplina              | 004.22  |
| Soggetti                | Computer architecture   |
| Lingua di pubblicazione | Inglese   |
| Formato                 | Materiale a stampa  |
| Livello bibliografico   | Monografia  |
| Note generali           | Description based upon print version of record.   |
| Nota di bibliografia    | Includes bibliographical references and index.  |
| Nota di contenuto       | pt. 1. Elements of a basic architecture -- pt. 2. Programming model and operation -- pt. 3. Memory hierarchy -- pt. 4. Parallelism and performance enhancement -- pt. 5. Appendices.  |
| Sommario/riassunto      | This book lays out the concepts necessary to understand how a computer works. For reasons of clarity, the authors have deliberately chosen examples that apply to machines from all eras, without having to water down the contents of the book. This choice helps to show how techniques, concepts and performances have evolved since the first computers. The book is divided into five parts. The first four, which are of increasing difficulty, are the core of the book: "Elements of a Basic Architecture", "Programming Model and Operation", "Memory Hierarchy", "Parallelism and Performance Enh |