1. Record Nr. UNINA9910818255303321 Autore Kulisch Ulrich Titolo Computer arithmetic and validity: theory, implementation, and applications / / Ulrich Kulisch Berlin; ; New York, : Walter De Gruyter, c2008 Pubbl/distr/stampa **ISBN** 1-282-19584-0 9786612195846 3-11-020319-7 Edizione [1st ed.] Descrizione fisica 1 online resource (428 p.) Collana De Gruyter studies in mathematics;; 33 Classificazione SK 900 Disciplina 004.0151 22 Soggetti Computer arithmetic Computer arithmetic and logic units Floating-point arithmetic Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Description based upon print version of record. Note generali Frontmatter -- Contents -- Introduction -- Chapter 1 First Concepts --Nota di contenuto Chapter 2 Ringoids and Vectoids -- Chapter 3 Definition of Computer Arithmetic -- Chapter 4 Interval Arithmetic -- Chapter 5 Floating-Point Arithmetic -- Chapter 6 Implementation of Floating-Point Arithmetic on a Computer -- Chapter 7 Hardware Support for Interval Arithmetic -- Chapter 8 Scalar Products and Complete Arithmetic -- Chapter 9 Sample Applications -- Backmatter Sommario/riassunto The present book deals with the theory of computer arithmetic, its implementation on digital computers and applications in applied mathematics to compute highly accurate and mathematically verified results. The aim is to improve the accuracy of numerical computing (by implementing advanced computer arithmetic) and to control the quality of the computed results (validity). The book can be useful as high-level

undergraduate textbook but also as reference work for scientists researching computer arithmetic and applied mathematics.