

1. Record Nr.	UNINA9910816483303321
Autore	Oooi Keat Gin <1959->
Titolo	"Baik, buruk, dan dahsyat" pelbagai wajah peperangan dan akibatnya terhadap Asia Tenggara pada kurun kedua puluh // Oooi Keat Gin
Pubbl/distr/stampa	Gelugor, Malaysia : , : Penerbit Universiti Sains Malaysia, , 2016 ©2010
ISBN	983-861-809-8
Descrizione fisica	1 online resource (100 pages)
Collana	Siri syarahan umum ; ; Pelantikan 2008/Bil. 2
Disciplina	363.3498
Soggetti	HISTORY - Asia - Southeast Asia Crimes against humanity War and society- History-20th century War and civilization-History-20th century
Lingua di pubblicazione	Malay
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Includes bibliographical references and index.

2. Record Nr.	UNINA9910437893303321
Autore	Gaggl Richard
Titolo	Delta-sigma A/D-converters : practical design for communication systems / / Richard Gaggl
Pubbl/distr/stampa	Berlin, : Springer, 2013
ISBN	3-642-34543-3 1-283-94633-5
Edizione	[1st ed. 2013.]
Descrizione fisica	1 online resource (156 p.)
Collana	Springer Series in Advanced Microelectronics, , 1437-0387
Disciplina	004.01/51 621.381/32
Soggetti	Analog-to-digital converters Digital-to-analog converters Modulators (Electronics) - Design
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Limitations of Delta-Sigma Converters -- A Delta-Sigma Converter with Dynamic-Biasing Technique -- A feed-forward Delta-Sigma Converter for ADSL -- A Delta-Sigma Converter for WLAN using a TEQ.
Sommario/riassunto	The emphasis of this book is on practical design aspects for broadband A/D converters for communication systems. The embedded designs are employed for transceivers in the field of ADSL solutions and WLAN applications. An area- and power-efficient realization of a converter is mandatory to remain competitive in the market. The right choice for the converter topology and architecture needs to be done very carefully to result in a competitive FOM. The book begins with a brief overview of basic concepts about ADSL and WLAN to understand the ADC requirements. At architectural level, issues on different modulator topologies are discussed employing the provided technology node. The design issues are pointed out in detail for modern digital CMOS technologies, beginning with 180nm followed by 130nm and going down to 65nm feature size. Beside practical aspects, challenges to mixed-signal design level are addressed to optimize the converters in terms of consumed chip area, power consumption and design for high yield in volume production. Thus, careful considerations on circuit- and

architectural- level are performed by introducing a dynamic-biasing technique, a feed-forward approach and a resolution in time instead of amplitude resolution.
