

1. Record Nr.	UNINA990000056710403321
Autore	Séguier, Guy
Titolo	L'electronique de puissance : les fonctions de base et leurs principales applications / Guy Segulier
Pubbl/distr/stampa	Paris : Dunod, 1985
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2. Record Nr.	UNINA9910813723703321
Autore	Dice Pete <1970->
Titolo	Quick boot : a guide for embedded firmware developers / / Pete Dice
Pubbl/distr/stampa	Boston : , : DeG Press, , [2018]
ISBN	1-5015-0672-2
Edizione	[Second edition.]
Descrizione fisica	1 online resource (282 pages)
Disciplina	004
Soggetti	Computer firmware Computer bootstrapping Basic input-output system
Lingua di pubblicazione	Inglese
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Nota di bibliografia	Includes bibliographical references.
Nota di contenuto	Frontmatter -- Acknowledgments -- Contents -- Foreword from the First Edition -- Chapter 1: System Firmware's Missing Link -- Chapter 2: Intel Architecture Basics -- Chapter 3: System Firmware Terms and Concepts -- Chapter 4: Silicon-Specific Initialization -- Chapter 5: Industry Standard Initialization -- Chapter 6: System Firmware Debug Techniques -- Chapter 7: Shells and Native Applications -- Chapter 8: Loading an Operating System -- Chapter 9: The Intel® Architecture Boot Flow -- Chapter 10: Bootstrapping Embedded -- Chapter 11: Intel's Fast Boot Technology -- Chapter 12: Collaborative Roles in Quick Boot -- Chapter 13: Legal Decisions -- Appendix A: Generating Serial Presence Detection Data for Down Memory Configurations -- Index
Sommario/riassunto	Quick Boot is designed to give developers a background in the basic architecture and details of a typical boot sequence. More specifically, this book describes the basic initialization sequence that allows developers the freedom to boot an OS without a fully featured system BIOS. Various specifications provide the basics of both the code bases and the standards. This book also provides insights into optimization techniques for more advanced developers. With proper background information, the required specifications on hand, and diligence, many developers can create quality boot solutions using this text. Pete Dice is Engineering Director of Verifone, where he manages OS Engineering

teams in Dublin, Ireland and Riga Latvia. Dice successfully launched Intel® Quark™, Intel's first generation SoC as well as invented the Intel® Galileo™ development board and developed a freemium SW strategy to scale Intel IoT gateway features across product lines. He is also credited with architecting the "Moon Island" software stack and business model.
