

1. Record Nr.	UNINA9910812748203321
Titolo	The future of computing performance : game over or next level? // Samuel H. Fuller and Lynette I. Millett, editors. ; Committee on Sustaining Growth in Computing Performance, Computer Science and Technology Board, Division on Engineering and Physical Science, National Research Council of the National Academies
Pubbl/distr/stampa	Washington, D.C., : National Academies Press, c2011
ISBN	0-309-21164-6 1-283-08152-0 9786613081520 0-309-15952-0
Edizione	[1st ed.]
Descrizione fisica	1 online resource (201 p.)
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Disciplina	004.24
Soggetti	Electronic digital computers - Evaluation Multiprocessors Technological innovations - Forecasting
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references.
Nota di contenuto	""Front Matter""; ""Preface""; ""Acknowledgment of Reviewers""; ""Contents""; ""Abstract""; ""Summary""; ""1 The Need for Continued Performance Growth""; ""2 What Is Computer Performance?""; ""3 Power Is Now Limiting Growth in Computing Performance""; ""4 The End of Programming as We Know It""; ""5 Research, Practice, and Education to Meet Tomorrow's Performance Needs""; ""Appendixes""; ""Appendix A: A History of Computer Performance""; ""Appendix B: Biographies of Committee Members and Staff"" ""Appendix C: Reprint of Gordon E. Moore's Cramming More Components onto Integrated Circuits?""""Appendix D: Reprint of Robert H. Dennard's Design of Ion-Implanted MOSFET's with Very Small Physical Dimensions""
Sommario/riassunto	The end of dramatic exponential growth in single-processor performance marks the end of the dominance of the single

microprocessor in computing. The era of sequential computing must give way to a new era in which parallelism is at the forefront. Although important scientific and engineering challenges lie ahead, this is an opportune time for innovation in programming systems and computing architectures. We have already begun to see diversity in computer designs to optimize for such considerations as power and throughput. The next generation of discoveries is likely to require advances at both the hardware and software levels of computing systems. There is no guarantee that we can make parallel computing as common and easy to use as yesterday's sequential single-processor computer systems, but unless we aggressively pursue efforts suggested by the recommendations in this book, it will be "game over" for growth in computing performance. If parallel programming and related software efforts fail to become widespread, the development of exciting new applications that drive the computer industry will stall; if such innovation stalls, many other parts of the economy will follow suit. The future of computing performance describes the factors that have led to the future limitations on growth for single processors that are based on complementary metal oxide semiconductor (CMOS) technology. It explores challenges inherent in parallel computing and architecture, including ever-increasing power consumption and the escalated requirements for heat dissipation --
