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Sommario/riassunto	This e-book provides several state-of-the-art analog circuit design techniques. It presents both empirical and theoretical materials for system-on-a-chip (SOC) circuit design. Fundamental communication concepts are used to explain a variety of topics including data conversion (ADC, DAC, S-? oversampling data converters), clock data recovery, phase-locked loops for system timing synthesis, supply voltage regulation, power amplifier design, and mixer design. This is an excellent reference book for both circuit designers and researchers who are interested in the field of design of analog communic