1. | | |
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| Record Nr. | UNINA9910796660003321 |
| Autore | Inchauste Gabriela |
| Titolo | The Distributional Impact of Taxes and Transfers : : Evidence From Eight Developing Countries / / Gabriela Inchauste |
| Pubbl/distr/stampa | Washington, D.C. : , : The World Bank, , 2017 |
| Descrizione fisica | 1 online resource (318 pages) |
| Collana | Directions in Development;Directions in Development - Poverty |
| Disciplina | 339.2 |
| Soggetti | Income distribution
Poverty - Government policy
Fiscal policy |
| Lingua di pubblicazione | Inglese |
| Formato | Materiale a stampa |
| Livello bibliografico | Monografia |
| Nota di contenuto | Overview: fiscal policy and redistribution / Gabriela Inchauste and Nora Lustig -- Fiscal incidence in Armenia / Stephen D. Younger, Artsvi Khachatryan -- Fiscal incidence analysis for Ethiopia / Ruth Hill, Gabriela Inchauste, Nora Lustig, Eyasu Tsehaye, Tassew Woldehanna -- The distributional impact of fiscal policy in Georgia / Cesar Cancho, Elena Bondarenko -- The distributional impact of fiscal policy in Indonesia / Jon Jellema, Matthew Wai-Poi, Rythia Afkar -- The distributional impact of fiscal policy in Jordan / Shamma A. Alam, Gabriela Inchauste, Umar Serajuddin -- Who benefits from fiscal redistribution in the Russian Federation? / Luis F. Lopez-Calva, Nora Lustig, Mikhail Matytsin, Daria Popova -- The distributional impact of fiscal policy in South Africa / Gabriela Inchauste, Nora Lustig, Mashekwa Maboshe, Catriona Purfield, Ingrid Woolard -- The Incidence of taxes and spending in Sri Lanka / Nisha Arunatilake, Gabriela Inchauste, Nora Lustig. |
| Sommario/riassunto | The World Bank has partnered with the Commitment to Equity Institute at Tulane University to implement their diagnostic tool-the Commitment to Equity (CEQ) Assessment-designed to assess how taxation and public expenditures affect income inequality, poverty, and different economic groups. The approach relies on comprehensive fiscal incidence analysis, which measures the contribution of each individual intervention to poverty and inequality reduction as well as |

the combined impact of taxes and social spending. The CEQ Assessment provide an evidence base upon which alternative reform options can be analyzed. The use of a common methodology makes the results comparable across countries. This volume presents eight country studies that examine the distributional effects of individual programs and policy measures-and the net effect of each country's mix of policies and programs. These case studies were produced in the context of Bank policy dialogue and have since been used to propose alternative reform options.

2.	Record Nr.	UNINA9910768476203321
	Titolo	Formal Methods for Hardware Verification : 6th International School on Formal Methods for the Design of Computer, Communication, and Software Systems, SFM 2006, Bertinoro, Italy, May 22-27, 2006, Advances Lectures / / edited by Marco Bernardo, Alessandro Cimatti
	Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2006
	ISBN	3-540-34305-9
	Edizione	[1st ed. 2006.]
	Descrizione fisica	1 online resource (VIII, 244 p.)
	Collana	Programming and Software Engineering, , 2945-9168 ; ; 3965
	Altri autori (Persone)	CimattiAlessandro BernardoMarco
	Disciplina	004.01/51
	Soggetti	Computer science Software engineering Compilers (Computer programs) Computers, Special purpose Computer networks Theory of Computation Software Engineering Compilers and Interpreters Computer Science Logic and Foundations of Programming Special Purpose and Application-Based Systems Computer Communication Networks
	Lingua di pubblicazione	Inglese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia

Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Hardware Design and Simulation for Verification -- Automatic Test Pattern Generation -- An Introduction to Symbolic Trajectory Evaluation -- BDD-Based Hardware Verification -- SAT-Based Verification Methods and Applications in Hardware Verification -- Building Efficient Decision Procedures on Top of SAT Solvers -- Refinement and Theorem Proving -- Floating-Point Verification Using Theorem Proving.
Sommario/riassunto	This book presents 8 papers accompanying the lectures of leading researchers given at the 6th edition of the International School on Formal Methods for the Design of Computer, Communication and Software Systems (SFM 2006). SFM 2006 was devoted to formal techniques for hardware verification and covers several aspects of the hardware design process, including hardware design languages and simulation, property specification formalisms, automatic test pattern generation, symbolic trajectory evaluation, and more.