

1. Record Nr.	UNINA9910796036903321
Autore	Blokh Alexander M. <1958->
Titolo	Fixed point theorems for plane continua with applications / / Alexander M. Blokh [and four others]
Pubbl/distr/stampa	Providence, Rhode Island : , : American Mathematical Society, , 2012 ©2012
ISBN	1-4704-1004-4
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Collana	Memoirs of the American Mathematical Society, , 0065-9266 ; ; Volume 224, Number 1053
Disciplina	515/.39
Soggetti	Fixed point theory
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	"Volume 224, Number 1053 (second of 4 numbers)."
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Nota di contenuto	""Chapter 4. Partitions of domains in the sphere""""4.1. Kulkarni-Pinkall Partitions""; ""4.2. Hyperbolic foliation of simply connected domains""; ""4.3. Schoenflies Theorem""; ""4.4. Prime ends""; ""Part 2 . Applications of Basic Theory""; ""Chapter 5. Description of main results of Part 2""; ""5.1. Outchannels""; ""5.2. Fixed points in invariant continua""; ""5.3. Fixed points in non-invariant continua æ?the case of dendrites""; ""5.4. Fixed points in non-invariant continua æ?the planar case""; ""5.5. The polynomial case""; ""Chapter 6. Outchannels and their properties"" ""6.1. Outchannels""""6.2. Uniqueness of the Outchannel""; ""Chapter 7. Fixed points""; ""7.1. Fixed points in invariant continua""; ""7.2. Dendrites""; ""7.3. Non-invariant continua and positively oriented maps of the plane""; ""7.4. Maps with isolated fixed points""; ""7.5. Applications to complex dynamics""; ""Bibliography""; ""Index""

2. Record Nr.	UNINA9910973267603321
Autore	Maxfield Clive <1957->
Titolo	The design warrior's guide to FPGAs : devices, tools, and flows / / Clive "Max" Maxfield
Pubbl/distr/stampa	Amsterdam ; ; London, : Newnes, c2004
ISBN	9786610745982 9781280745980 1280745983 9780080477138 0080477135
Edizione	[1st edition]
Descrizione fisica	1 online resource (561 p.)
Disciplina	621.395
Soggetti	Field programmable gate arrays Gate array circuits
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	One CD-ROM in pocket attached to inside back cover. Includes index.
Nota di contenuto	Front Cover; Copyright; Table of Contents; Preface; Acknowledgments; 1. Introduction; 2. Fundamental Concepts; 3. The Origin of FPGAs; 4. Alternative FPGA Architectures; 5. Programming SConfiguringw an FPGA; 6. Who Are All the Players?; 7. FPGA Versus ASIC Design Styles; 8. Schematic-Based Design Flows; 9. HDL-Based Design Flows; 10. Silicon Virtual Prototyping for FPGAs; 11. C/Cbb etc.-Based Design Flows; 12. DSPIBased Design Flows; 13. Embedded Processor-Based Design Flows; 14. Modular and Incremental Design; 15. High-Speed Design and Other PCB Considerations 16. Observing Internal Nodes in an FPGA17. Intellectual Property; 18. Migrating ASIC Designs to FPGAs and Vice Versa; 19. SimulationI SynthesisI VerificationI etcB Design Tools; 20. Choosing the Right Device; 21. Gigabit Transceivers; 22. Reconfigurable Computing; 23. Field-Programmable Node Arrays; 24. Independent Design Tools; 25. Creating an Open-Source-Based Design Flow; 26. Future FPGA Developments; Appendix A: Signal Integrity 101; Appendix B: Deep-Submicron Delay Effects 101; Appendix C: Linear Feedback Shift

Sommario/riassunto

Field Programmable Gate Arrays (FPGAs) are devices that provide a fast, low-cost way for embedded system designers to customize products and deliver new versions with upgraded features, because they can handle very complicated functions, and be reconfigured an infinite number of times. In addition to introducing the various architectural features available in the latest generation of FPGAs, The Design Warrior's Guide to FPGAs also covers different design tools and flows. This book covers information ranging from schematic-driven entry, through traditional HDL/RTL-based simulation and lo
