1. Record Nr. UNINA9910464781803321

Titolo	Evidence-based management of diabetes / / [edited by] Jiten Vora, John Buse
Pubbl/distr/stampa	Harley : , : tfm Publishing, , 2012
ISBN	1-908986-69-7 1-908986-71-9
Edizione	[First edition.]
Descrizione fisica	1 online resource (520 p.)
Altri autori (Persone)	VoraJiten BuseJohn B
Disciplina	616.4/62
Soggetti	Diabetes - Treatment Evidence-based medicine Electronic books.
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	""COVER FRONT""; ""Contents""; ""Foreword""; ""Contributors""; ""Acknowledgements""; ""Using evidence-based medicine""; ""Chapter 1 Where does insulin resistance start; contribution to the development of diabetes?""; ""Chapter 2 Genetics studies in diabetes a€? how do they help?""; ""Chapter 3 Prevention of type 2 diabetes in high-risk individuals: achievable?""; ""Chapter 4 The glucocentric approach to diabetes care""; ""Chapter 5 Supplying the missing link in diabetes care: evidence-based structured education for people with type 2 diabetes"" ""Chapter 6 The role of blood glucose monitoring in type 2 diabetes a plea for common sense?""; ""Chapter 7 The role of continuous glucose monitoring in the management of type 1 and type 2 diabetes""; ""Chapter 8 Treatment of diabetes through targeting weight loss""; ""Chapter 9 Weight loss therapies in diabetes a€? is there a future?""; ""Chapter 10 Goals of treatment in type 2 diabetes mellitus: I²-cell preservation""; ""Chapter 11 Early insulin initiation in type 2 diabetes""; ""Chapter 12 Immune intervention in type 1 diabetes"" ""Chapter 13 Pancreas and islet transplantation: an evidence-based analysis""""Chapter 14 Hypoglycemia in type 2 diabetes: does it matter?""; ""Chapter 15 Mental illness and diabetes a€? intricate

		interplay""; ""Chapter 16 Diabetes treatments and cancer""; ""Chapter 17 Screening for diabetic retinopathy""; ""Chapter 18 Medical therapies for diabetic eye disease a€? how good are they?""; ""Chapter 19 Diabetic neuropathic pain a€? assessment and treatment""; ""Chapter 20 The diabetic foot: where are we?"" ""Chapter 21 Non-alcoholic fatty liver disease (NAFLD) in diabetes: distraction or impending disaster?""""Chapter 22 Management of kidney risk in diabetes""; ""Chapter 23 Use of dual RAAS blockade in diabetic nephropathy""; ""Chapter 24 Glucose control and macrovascular disease/morbidity""; ""Chapter 25 Hyperglycemia in acute coronary syndromes""; ""Chapter 26 Screening for coronary artery disease in patients with type 2 diabetes""; ""Index""; ""COVER BACK""
2.	Record Nr.	UNINA9910456662303321
	Autore	Yiu Joseph
	Titolo	The definitive guide to the ARM Cortex-M3 [[electronic resource] /] / Joseph Yiu
	Pubbl/distr/stampa	Amsterdam ; ; Boston, : Newnes, c2010
	ISBN	1-282-75584-6 9786612755842 1-85617-964-8
	Edizione	[2nd ed.]
	Descrizione fisica	1 online resource (481 p.)
	Disciplina	621.39/16
	Soggetti	Embedded computer systems Microprocessors Electronic books.
	Lingua di pubblicazione	Inglese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
	Note generali	Description based upon print version of record.
	Nota di bibliografia	Includes bibliographical references and index.
	Nota di contenuto	Front Cover; Half Title Page; The Definitive Guide to the ARM Cortex-M3; Copyright Page; Table of Contents; Foreword; Foreword; Preface; Acknowledgments; Conventions; Terms and Abbreviations; Chapter 1. Introduction; 1.1 What Is the ARM Cortex-M3 Processor?; 1.2 Background of ARM and ARM Architecture; 1.2.1 A Brief History; 1.2.2 Architecture Versions; 1.2.3 Processor Naming; 1.3 Instruction Set Development; 1.4 The Thumb-2 Technology and Instruction Set

Architecture; 1.5 Cortex-M3 Processor Applications; 1.6 Organization of This Book; 1.7 Further Reading; Chapter 2. Overview of the Cortex-M3
2.1 Fundamentals2.2 Registers; 2.2.1 R0-R12: General-Purpose Registers; 2.2.2 R13: Stack Pointers; 2.2.3 R14: The Link Register; 2.2.4 R15: The Program Counter; 2.2.5 Special Registers; 2.3 Operation Modes; 2.4 The Built-In Nested Vectored Interrupt Controller; 2.4.1 Nested Interrupt Support; 2.4.2 Vectored Interrupt Support; 2.4.3 Dynamic Priority Changes Support; 2.4.4 Reduction of Interrupt Latency; 2.4.5 Interrupt Masking; 2.5 The Memory Map; 2.6 The Bus Interface; 2.7 The MPU; 2.8 The Instruction Set; 2.9 Interrupts and Exceptions; 2.9.1 Low Power and High Energy Efficiency
2.10 Debugging Support2.11 Characteristics Summary; 2.11.1 High Performance; 2.11.2 Advanced Interrupt-Handling Features; 2.11.3 Low Power Consumption; 2.11.4 System Features; 2.11.5 Debug Supports; Chapter 3. Cortex-M3 Basics; 3.1 Registers; 3.1.1 General Purpose Registers R0 through R7; 3.1.2 General Purpose Registers R8 through R12; 3.1.3 Stack Pointer R13; 3.1.4 Link Register R14; 3.1.5 Program Counter R15; 3.2 Special Registers; 3.2.1 Program Status Registers; 3.2.2 PRIMASK, FAULTMASK, and BASEPRI Registers; 3.2.3 The Control Register; 3.3 Operation Mode; 3.4 Exceptions and Interrupts
3.5 Vector Tables3.6 Stack Memory Operations; 3.6.1 Basic Operations of the Stack; 3.6.2 Cortex-M3 Stack Implementation; 3.6.3 The Two-Stack Model in the Cortex-M3; 3.7 Reset Sequence; Chapter 4. Instruction Sets; 4.1 Assembly Basics; 4.1.1 Assembler Language: Basic Syntax; 4.1.2 Assembler Language: Use of Suffixes; 4.1.3 Assembler Language: Unified Assembler Language; 4.2 Instruction List; 4.2.1 Unsupported Instructions; 4.3 Instruction Descriptions; 4.3.1 Assembler Language: Moving Data; 4.3.2 LDR and ADR Pseudo-Instructions; 4.3.3 Assembler Language: Processing Data
4.3.4 Assembler Language: Call and Unconditional Branch4.3.5 Assembler Language: Decisions and Conditional Branches; 4.3.6 Assembler Language: Combined Compare and Conditional Branch; 4.3.7 Assembler Language: Instruction Barrier and Memory Barrier Instructions; 4.3.8 Assembly Language: Saturation Operations; 4.4 Several Useful Instructions in the Cortex-M3; 4.4.1 MSR and MRS; 4.4.2 More on the IF-THEN Instruction Block; 4.4.3 SDIV and UDIV; 4.4.4 REV, REVH, and REVSH; 4.4.5 Reverse Bit; 4.4.6 SXTB, SXTH, UXTB, and UXTH; 4.4.7 Bit Field Clear and Bit Field Insert; 4.4.8 UBFX and SBFX 4.4.9 LDRD and STRD

Sommario/riassunto

This user's guide does far more than simply outline the ARM Cortex-M3 CPU features; it explains step-by-step how to program and implement the processor in real-world designs. It teaches readers how to utilize the complete and thumb instruction sets in order to obtain the best functionality, efficiency, and reuseability. The author, an ARM engineer who helped develop the core, provides many examples and diagrams that aid understanding. Quick reference appendices make locating specific details a snap! Whole chapters are dedicated to: Debugging using the new CoreSight technologyMi

3. Record Nr. UNINA9910139051303321

Autore	Hammer-Krabbe Stephanie <1974->
Titolo	Architects of electronic trading [[electronic resource]] : technology leaders who are shaping today's financial markets / / Stephanie Hammer-Krabbe
Pubbl/distr/stampa	Hoboken, NJ, : Wiley, c2013
ISBN	1-118-70547-5 1-118-48810-5
Edizione	[1st edition]
Descrizione fisica	1 online resource (210 p.)
Collana	Wiley trading series
Disciplina	332.64/20285
Soggetti	Electronic trading of securities Investments - Computer network resources
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Includes index.
Nota di contenuto	Foreword -- Acknowledgments -- About the author -- Meet the architects -- Business challenges -- Laying a strong foundation with transformational technology -- Hardware acceleration with FPGAS -- Conclusion: a call for continued education -- Index.
Sommario/riassunto	Insights that can help you improve your technology edge Featuring contributions from technology visionaries at leading alternative investors, hedge funds, trading firms, exchanges, and vendors, this book covers current trends in trading technology. The book features interviews with the leaders responsible for the technology that is shaping today's electronic financial markets. You'll hear the views of CIOs, CTOs, and other technology leaders on emerging technologies, innovation in the financial sector, and how technology is enhancing markets in ways other than just speed. Their perspective

4. Record Nr. UNINA9910795920603321

Titolo	At the nexus of cybersecurity and public policy : some basic concepts and issues / / David Clark, Thomas Berson, and Herbert S. Lin, editors ; Committee on Developing a Cybersecurity Primer: Leveraging Two Decades of National Academies Work, Computer Science and Telecommunications Board, National Research Council of the Academies
Pubbl/distr/stampa	Washington, District of Columbia : , : The National Academies Press, , 2014 ©2014
ISBN	0-309-30321-4 0-309-30319-2
Descrizione fisica	1 online resource (149 p.)
Disciplina	005.80973
Soggetti	Computer security - United States Computer networks - Security measures - Government policy Computer crimes - Prevention
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references.
Nota di contenuto	""Front Matter""; ""Preface""; ""Acknowledgment of Reviewers""; ""Contents""; ""Summary""; ""1 Why Care About Cybersecurity?""; ""2 Some Basics of Computing and Communications Technology and Their Significance for Cybersecurity""; ""3 On the Nature of Cybersecurity""; ""4 Enhancing Cybersecurity""; ""5 Tensions Between Cybersecurity and Other Public Policy Concerns""; ""6 Findings and Conclusion""; ""Appendixes""; ""Appendix A: Committee Members and Staff""; ""Appendix B: Bibliography""
Sommario/riassunto	"According to this report, cybersecurity is a never-ending battle; threats will evolve as adversaries adopt new tools and techniques to compromise security. Cybersecurity is therefore an ongoing process that needs to evolve as new threats are identified. At the Nexus of Cybersecurity and Public Policy is a call for action to make cybersecurity a public safety priority. For a number of years, the cybersecurity issue

has received increasing public attention; however, most policy focus has been on the short-term costs of improving systems. In its explanation of the fundamentals of cybersecurity and the discussion of potential policy responses."--