

1. Record Nr.	UNISALENT0991004086189707536
Autore	Landsheere, Gilbert : de
Titolo	Il pilotaggio dei sistemi educativi / Gilbert de Landsheere ; introduzione all'edizione italiana di Gaetano Domenici
Pubbl/distr/stampa	Roma : Armando, c1998
ISBN	8871447891
Descrizione fisica	175 p. ; 24 cm.
Collana	I problemi dell'educazione
Altri autori (Persone)	Domenici, Gaetano
Disciplina	379.154
Soggetti	Didattica - Valutazione Educazione - Valutazione
Lingua di pubblicazione	Italiano
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Trad. e note di Andrea Gentile.

2. Record Nr.	UNINA9910788523903321
Autore	Garcia Pascual Antonio
Titolo	Toward An Effective Supervision of Partially Dollarized Banking Systems // Antonio Garcia Pascual, Jorge Cayazzo, Socorro Heysen, Eva Gutierrez
Pubbl/distr/stampa	Washington, D.C. : , : International Monetary Fund, , 2006
ISBN	1-4623-0542-3 1-4527-1181-X 1-282-44801-3 9786613821201 1-4519-0828-8
Descrizione fisica	1 online resource (50 p.)
Collana	IMF Working Papers
Altri autori (Persone)	CayazzoJorge HeysenSocorro GutierrezEva
Soggetti	Banks and banking - State supervision Banks and banking - Risk management Banks and Banking Foreign Exchange Money and Monetary Policy Financial Institutions and Services: Government Policy and Regulation Financing Policy Financial Risk and Risk Management Capital and Ownership Structure Value of Firms Goodwill Banks Depository Institutions Micro Finance Institutions Mortgages Monetary Systems Standards Regimes Government and the Monetary System Payment Systems Financial services law & regulation Banking Monetary economics

Currency
Foreign exchange
Credit risk
Currencies
Liquidity risk
Exchange rates
Financial regulation and supervision
Money
Exchange rate risk
Financial risk management
Banks and banking
Peru

Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	"January 2006."
Nota di bibliografia	Includes bibliographical references.
Nota di contenuto	""Contents""; ""I. INTRODUCTION""; ""II. SUPERVISORY PRACTICES""; ""III. TOWARDS GOOD PRACTICES""; ""COUNTRY PRACTICES: SURVEY RESULTS""; ""CURRENCY-INDUCED CREDIT RISK IN SELECTED BANKING SYSTEMS""; ""References""
Sommario/riassunto	The paper presents a supervisory framework that addresses the vulnerabilities of partially dollarized banking systems. The tendency to underprice systemic liquidity risk and currency-induced credit risk creates vulnerabilities that need supervisory responses. The framework seeks to induce agents to better internalize risks by implementing a risk based approach to supervision, following the risk management guidelines of the Basel Committee, and by establishing buffers to cover higher liquidity and solvency risks. The paper also shows that most dollarized countries have addressed their liquidity vulnerabilities, but few have addressed those arising from currency-induced credit risks.

3. Record Nr.	UNINA9910760293703321
Autore	Roy Shirshendu
Titolo	Advanced Digital System Design : A Practical Guide to Verilog Based FPGA and ASIC Implementation // by Shirshendu Roy
Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2024
ISBN	3-031-41085-8
Edizione	[1st ed. 2024.]
Descrizione fisica	1 online resource (0 pages)
Disciplina	621.381
Soggetti	Electronic circuits Computers Computer-aided engineering Electrical engineering Electronic Circuits and Systems Computer Hardware Computer-Aided Engineering (CAD, CAE) and Design Electrical and Electronic Engineering
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di contenuto	Binary Number System -- Basics of Verilog HDL -- Basic Combinational Circuits -- Basic Sequential Circuits -- Memory Design -- Finite State Machines -- Design of Adder Circuits -- Design of Multiplier Circuits -- Division and Modulous Operation -- Square Root and its Reciprocal -- CORDIC Algorithm -- Floating Point Architectures -- Timing Analysis -- Digital System Implementation -- Low Power Digital System Design -- Digital System Design Examples.
Sommario/riassunto	The book is designed to serve as a textbook for courses offered to undergraduate and graduate students enrolled in electrical, electronics, and communication engineering. The objective of this book is to help the readers to understand the concepts of digital system design as well as to motivate the students to pursue research in this field. Verilog Hardware Description Language (HDL) is preferred in this book to realize digital architectures. Concepts of Verilog HDL are discussed in a separate chapter and many Verilog codes are given in this book for

better understanding. Concepts of system Verilog to realize digital hardware are also discussed in a separate chapter. The book covers basic topics of digital logic design like binary number systems, combinational circuit design, sequential circuit design, and finite state machine (FSM) design. The book also covers some advanced topics on digital arithmetic like design of high-speed adders, multipliers, dividers, square root circuits, and CORDIC block. The readers can learn about FPGA and ASIC implementation steps and issues that arise at the time of implementation. One chapter of the book is dedicated to study the low-power design techniques and another to discuss the concepts of static time analysis (STA) of a digital system. Design and implementation of many digital systems are discussed in detail in a separate chapter. In the last chapter, basics of some advanced FPGA design techniques like partial re-configuration and system on chip (SoC) implementation are discussed. These designs can help the readers to design their architecture. This book can be very helpful to both undergraduate and postgraduate students and researchers.
