1. Record Nr. UNINA9910783249203321 Autore Ranter Carl De Titolo High Data Rate Transmitter Circuits [[electronic resource]]: RF CMOS Design and Techniques for Design Automation / / by Carl De Ranter, Michiel Stevaert Boston, MA:,: Springer US,, 2003 Pubbl/distr/stampa 0-306-48713-6 **ISBN** Descrizione fisica 1 online resource (XVIII, 227 p.) Collana The International Series in Engineering and Computer Science, Analog Circuits and Signal Processing, , 0893-3405; ; 747 Disciplina 621.3815 Soggetti Engineering Computer-aided engineering Manufacturing industries Machines **Tools** Electrical engineering Electronic circuits Circuits and Systems **Electrical Engineering** Computer-Aided Engineering (CAD, CAE) and Design Manufacturing, Machines, Tools Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Nota di contenuto List of Figures -- List of Tables -- 1: Introduction. 1.1. Some Observations. 1.2. To CMOS or Not to CMOS. 1.3. Covered Topics -- 2: RF Modeling, Quadrature Generation and Flip-Chip Bonding. 2.1. Introduction, 2.2. Influence of Substrate Resistivity on RF-designs, 2.3. RF-Modeling. 2.4. Quadrature LO Generation. 2.5. Manufacturing. 2.6.

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## Sommario/riassunto

High Data Rate Transmitter Circuits is a practical guide and introduction to the design of key RF building blocks used in high data rate transmitters. The emphasis lies on CMOS circuit techniques applicable to oscillators and upconvertors. Furthermore, a method for RF-specific design automation is exemplified by the CYCLONE tool for automated LC-VCO synthesis. Written in an easily accessible manner, High Data Rate Transmitter Circuits is essential reading for both students and practicing engineers interested in analog RF design and RF-specific design automation. The book has been praised for its pleasant and light style of writing, without losing detail on the technical side.