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	Autore	Lee James M. <1962->
	Titolo	Verilog Quickstart [[electronic resource]] : a practical guide to simulation and synthesis in Verilog / / James M. Lee
	Pubbl/distr/stampa	Boston, : Kluwer Academic Publishers, 2002
	ISBN	1-280-20636-5
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	Edizione	[3rd ed.]
	Descrizione fisica	1 online resource (378 p.)
	Collana	The Kluwer international series in engineering and computer science ; ; SECS 667
	Disciplina	621.39/2
	Soggetti	Verilog (Computer hardware description language)
	Lingua di pubblicazione	Inglese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
	Note generali	Includes index.
	Nota di contenuto	to the Verilog Language -- Structural Modeling -- Starting Procedural Modeling -- System Tasks for Displaying Results -- Data Objects -- Procedural Assignments -- Operators -- Creating Combinatorial and Sequential Logic -- Procedural Flow Control -- Tasks and Functions -- Advanced Procedural Modeling -- User-Defined Primitives -- Parameterized Modules -- State Machines -- Modeling Tips -- Modeling Style Trade-Offs -- Test Benches and Test Management -- Model Organization -- Common Errors -- Debugging a Design -- Code Coverage.
	Sommario/riassunto	From a review of the Second Edition 'If you are new to the field and want to know what "all this Verilog stuff is about," you've found the golden goose. The text here is straight forward, complete, and example rich -mega-multi-kudos to the author James Lee. Though not as detailed as the Verilog reference guides from Cadence, it likewise doesn't suffer from the excessive abstractness those make you wade through. This is a quick and easy read, and will serve as a desktop reference for as long as Verilog lives. Best testimonial: I'm buying my fourth and fifth copies tonight (I've loaned out/lost two of my others).' Zach Coombes, AMD.