Record Nr. UNINA9910777927603321 Autore Singh Ravendra Titolo Design and implementation of compiler [[electronic resource] /] / Ravendra Singh, Vivek Sharma, Manish Varshney New Delhi, : New Age International (P) Ltd., Publishers, c2009 Pubbl/distr/stampa **ISBN** 1-282-24081-1 9786612240812 81-224-2865-7 Descrizione fisica 1 online resource (423 p.) Altri autori (Persone) SharmaVivek VarshenyManish 005.453 Disciplina 415.028542 Soggetti Compilers (Computer programs) Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Note generali Description based upon print version of record. Includes bibliographical references. Nota di bibliografia Nota di contenuto Cover; Preface; Acknowledgements; Contents; Chapter 1 Introduction to Language; Chapter 2 Introduction to Compiler; Chapter 3 Introduction to Source Code and Lexical Analysis; Chapter 4 Syntax Analysis and Directed Translation: Chapter 5 Semantic Analysis: Chapter 6 Three Address Code Generation; Chapter 7 Code Optimization; Chapter 8 Code Generation; Chapter 9 Symbol Table; Chapter 10 Run Time Management; Chapter 11 Error Detection and Recovery; Chapter 12 Compiler Projects; Appendix-A: Code; Appendix-B: Directed Acyclic Graphs (DAGs) Sommario/riassunto About the Book: This well-organized text provides the design techniques of complier in a simple and straightforward manner. It describes the complete development of various phases of complier with their imitation of C language in order to have an understanding of their application. Primarily designed as a text for undergraduate students of Computer Science and Information Technology and postgraduate students of MCA. Key Features: Chapter1 covers all formal languages with their properties. More illustration on parsing to offer

enhanced perspective of parser and also more examples in e