

1. Record Nr.	UNINA9910768192403321
Titolo	Verification, Model Checking, and Abstract Interpretation : Third International Workshop, VMCAI 2002, Venice, Italy, January 21-22, 2002, Revised Papers / / edited by Agostino Cortesi
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2002
ISBN	3-540-47813-2
Edizione	[1st ed. 2002.]
Descrizione fisica	1 online resource (VIII, 331 p.)
Collana	Lecture Notes in Computer Science, , 1611-3349 ; ; 2294
Disciplina	005.1/4
Soggetti	Computer science Software engineering Compilers (Computer programs) Theory of Computation Software Engineering Computer Science Computer Science Logic and Foundations of Programming Compilers and Interpreters
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Security and Protocols -- Combining Abstract Interpretation and Model Checking for Analysing Security Properties of Java Bytecode -- Proofs Methods for Bisimulation Based Information Flow Security -- A Formal Correspondence between Offensive and Defensive JavaCard Virtual Machines -- Analyzing Cryptographic Protocols in a Reactive Framework -- Timed Systems and Games -- An Abstract Schema for Equivalence-Checking Games -- Synchronous Closing of Timed SDL Systems for Model Checking -- Automata-Theoretic Decision of Timed Games -- Static Analysis -- Compositional Termination Analysis of Symbolic Forward Analysis -- Combining Norms to Prove Termination -- Static Monotonicity Analysis for $\omega$ -definable Functions over Lattices -- A Refinement of the Escape Property -- Optimizations -- Storage Size Reduction by In-place Mapping of Arrays -- Verifying BDD Algorithms through Monadic Interpretation -- Improving the Encoding

of LTL Model Checking into SAT -- Types and Verification -- Automatic Verification of Probabilistic Free Choice -- An Experiment in Type Inference and Verification by Abstract Interpretation -- Weak Muller Acceptance Conditions for Tree Automata -- A Fully Abstract Model for Higher-Order Mobile Ambients -- Temporal Logics and Systems -- A Simulation Preorder for Abstraction of Reactive Systems -- Approximating ATL\* in ATL -- Model Checking Modal Transition Systems Using Kripke Structures -- Parameterized Verification of a Cache Coherence Protocol: Safety and Liveness.

## Sommario/riassunto

This volume contains the revised version of papers presented at VMCAI 2002, the Third International Workshop on Verification, Model Checking, and Abstract Interpretation, Venice (Italy), January 21-22, 2002.

The main goal of the workshop was to give an overview of the main directions decisive for the growth and cross-fertilization of major research activities in program analysis and verification.

The VMCAI series was started in 1997 with the aim of gathering researchers interested in investigating similarities and differences among these three research methodologies, that may be summarized as follows: -

- program verification aims at proving that programs meet their specifications, i.e., that the actual program behavior corresponds to the desired one. -

- model checking is a specific approach to the verification of temporal properties of reactive and concurrent systems, which has been very successful in the area of finite-state programs. - abstract interpretation is a method for designing and comparing semantics of program, expressing various types of program properties; in particular, it has been successfully used to infer run-time program properties that can be valuable in optimizing programs.

The program committee selected 22 papers out of 41 submissions on the basis of at least 3 reviews. The principal selection criteria were relevance, quality, and clarity. The resulting volume offers the reader an interesting perspective of the current research trends in the area. In particular, the papers contribute to the following topics: Security and Protocols, Timed Systems and Games, Static Analysis, Optimizations, Types and Verification, and Temporal Logics and Systems. The quality of the papers, the interesting discussions at the workshop, and the friendly atmosphere enjoyed by all participants in Venice, encouraged us in the project of making VMCAI an annual privileged forum for researchers in the area. Special thanks are due to the institutions that sponsored the event: the Computer Science Department of the University Ca' Foscari, the European Association for Programming Languages and Systems (EAPLS), the MIUR Project "Interpretazione Astratta, Type System e Analisi Control-Flow" and the MIUR Project "Metodi Formali per la Sicurezza-MEFISTO". We are especially grateful to C. Braghin for her helpful support in organizing the workshop.