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	Nota di contenuto	HPPC 2007: Workshop on Highly Parallel Processing on a Chip -- HPPC 2007: Workshop on Highly Parallel Processing on a Chip -- Toward Realizing a PRAM-on-a-Chip Vision -- Societies of Cores and Their Computing Culture -- Hardware Transactional Memory with Operating System Support, HTMOS -- Auto-parallelisation of Sieve C++ Programs -- Adaptive L2 Cache for Chip Multiprocessors -- On-Chip COMA

Cache-Coherence Protocol for Microgrids of Microthreaded Cores -- Parallelization of Bulk Operations for STL Dictionaries -- UNICORE Summit 2007 -- UNICORE Summit 2007 -- A Black-Box Approach to Performance Analysis of Grid Middleware -- UNICORE/w3 -- Chemomentum - UNICORE 6 Based Infrastructure for Complex Applications in Science and Technology -- Flexible Streaming Infrastructure for UNICORE -- Extending UNICORE 5 Authentication Model by Supporting Proxy Certificate Profile Extensions -- Using SAML-Based VOMS for Authorization within Web Services-Based UNICORE Grids -- Attributes and VOs: Extending the UNICORE Authorisation Capabilities -- A Business-Oriented Grid Workflow Management System -- VHPC 2007: Workshop on Virtualization/Xen in High-Performance Cluster and Grid Computing -- VHPC 2007: Workshop on Virtualization/Xen in High-Performance Cluster and Grid Computing -- Virtualization Techniques in Network Emulation Systems -- SOA Based Control Plane for Virtual Clusters -- Grid Virtual Laboratory Architecture -- Information Service of Virtual Machine Pool for Grid Computing -- Virtual Cluster Management with Xen -- Deploying and Managing Xen Sites with XSM -- Xen Management with SmartFrog -- Integrating Xen with the Quattor Fabric Management System -- Getting 10 Gb/s from Xen: Safe and Fast Device Access from Unprivileged Domains.

| Sommario/riassunto | Parallelanddistributedprocessing, althoughwithinthefocusofcomputerscience researchforalongtime, isgainingmoreandmoreimportanceinawidespectrum of applications. These proceedings aim to demonstrate the use of parallel and distributed processing concepts in di?erent application ?elds, and attempt to spark interest in novel research directions to advance the embracing model of high-performance computing research in general. The objective of these workshops is to speci?cally address researchers c- ing from university, industry and governmental research organizations and application-oriented companies, in order to close the gap between purely s- enti?c research and the applicability of the research ideas to real-life problems. Euro-Par is an annual series of international conferences dedicated to the promotionandadvancementofallaspectsofparallelanddistributedcomputing. The 2007 event was the 13th issue of the conference. Euro-Par has for a long time been eager to attract colocated events sharing the same goal of promoting the development of parallel and distributed computing, both as an industrial technique and an academic discipline, extending the frontier of both the state of the art and the state of the practice. Since 2006, Euro-Par o?ers researchersthe chance to colocate advanced technical workshops back-to-back with the main conference. This is for a mutual bene?t: the workshops can take advantage of all technical and social facilities which are set up for the conference, so that the organizational tasks are kept to a minimal level; the conference can rely on workshopstoexperimentwithspeci?careasofresearchwhicharenotyetmature enough, or too speci?c, to lead to an o?cial, full-?edged topic at the conference. |