1. Record Nr. UNISA996396430803316

Autore	Barker Jane
Titolo	Poetical recreations [[electronic resource]] : consisting of original poems, songs, odes, &c. with several new translations : in two parts / / part I, occasionally written by Mrs. Jane Barker, part II, by several gentlemen of the universities, and others
Pubbl/distr/stampa	London, : Printed for Benjamin Crayle ..., 1688
Descrizione fisica	[24], 107, [9], 287, [1] p
Soggetti	English poetry - Early modern, 1500-1700 Songs, English
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Pt. 2 has special t.p.: Miscellanea, or, The second part of poetical recreations. Errata: p. [24]. Advertisement: p. [1] at end. Reproduction of original in British Library.
Sommario/riassunto	eebo-0018

2.

Record Nr.	UNISALENTO991001952479707536
Autore	Ward, Colin
Titolo	Anarchia come organizzazione / Colin Ward
Pubbl/distr/stampa	Milano : Antistato, 1979
Descrizione fisica	207 p. ; 19 cm.
Collana	Anarchismo oggi ; 1
Disciplina	320.57
Soggetti	Anarchismo
Lingua di pubblicazione	Italiano
Formato	Materiale a stampa
Livello bibliografico	Monografia

3.

Record Nr.	UNINA9910760250003321
Autore	LaMeres Brock J.
Titolo	Introduction to Logic Circuits & Logic Design with VHDL / / by Brock J. LaMeres
Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2024
ISBN	9783031425479 3031425472
Edizione	[3rd ed. 2024.]
Descrizione fisica	1 online resource (544 pages)
Disciplina	371.320973
Soggetti	Electronic circuits Microprocessors Computer architecture Logic design Electronic Circuits and Systems Processor Architectures Logic Design
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia

Note generali	Includes index.
Nota di contenuto	Introduction -- Analog Vs. Digital -- Number Systems -- Digital Circuitry & Interfacing -- Combinational Logic Design -- VHDL (Part 1) -- MSI Logic -- Sequential Logic Design -- VHDL (Part 2) -- Behavioral Modeling Of Sequential Logic -- Memory -- Programmable Logic -- Arithmetic Circuits -- Computer System Design -- Floating-Point Systems.
Sommario/riassunto	This textbook introduces readers to the fundamental hardware used in modern computers. The only pre-requisite is algebra, so it can be taken by college freshman or sophomore students or even used in Advanced Placement courses in high school. This book presents both the classical approach to digital system design (i.e., pen and paper) in addition to the modern hardware description language (HDL) design approach (computer-based). This textbook enables readers to design digital systems using the modern HDL approach while ensuring they have a solid foundation of knowledge of the underlying hardware and theory of their designs. This book is designed to match the way the material is actually taught in the classroom. Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics. The author has designed the content with learning goals and assessment at its core. Each section addresses a specific learning outcome that the learner should be able to “do” after its completion. The concept checks and exercise problems provide a rich set of assessment tools to measure learner performance on each outcome. This book can be used for either a sequence of two courses consisting of an introduction to logic circuits (Chapters 1-7) followed by logic design (Chapters 8-14) or a single, accelerated course that uses the early chapters as reference material. Written the way the material is taught, enabling a bottom-up approach to learning which culminates with a high-level of learning, with a solid foundation; Emphasizes examples from which students can learn: contains a over 250+ worked examples for nearly every section in the book; Includes more than 1000 exercise problems, as well as 70+ concept check questions for each section, tied directly to specific learning outcomes.