1. Record Nr. UNISA996466116403316

Titolo	Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation [[electronic resource]] : 16th International Workshop, PATMOS 2006, Montpellier, France, September 13-15, 2006, Proceedings / / edited by Johan Vounckx, Nadine Azemard, Philippe Maurine
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2006
ISBN	3-540-39097-9
Edizione	[1st ed. 2006.]
Descrizione fisica	1 online resource (XVI, 677 p.)
Collana	Theoretical Computer Science and General Issues, , 2512-2029 ; ; 4148
Classificazione	SS 4800
Disciplina	621.395
Soggetti	Computer science Logic design Microprocessors Computer architecture Electronic digital computers—Evaluation Computer arithmetic and logic units Computer storage devices Memory management (Computer science) Theory of Computation Logic Design Processor Architectures System Performance and Evaluation Arithmetic and Logic Structures Computer Memory Structure
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Session 1 – High-Level Design -- Session 2 – Power Estimation / Modeling -- Session 3 – Memory and Register Files -- Session 4 – Low-Power Digital Circuits -- Session 5 – Busses and Interconnects -- Session 6 – Low Power Techniques -- Session 7 – Applications and SoC Design -- Session 8 – Modeling -- Session 9 – Digital Circuits --

Session 10 – Reconfigurable and Programmable Devices -- Poster 1 -- Poster 2 -- Poster 3 -- Keynotes -- Industrial Session.

Sommario/riassunto

Welcome to the proceedings of PATMOS 2006, the 16th in a series of international workshops. PATMOS 2006 was organized by LIRMM with CAS technical - sponsorship and CEDA sponsorship. Over the years, the PATMOS workshop has evolved into an important European event, where researchers from both industry and academia discuss and investigate the emerging challenges in future and contemporary applications, design methodologies, and tools required for the development of upcoming generations of integrated circuits and systems. The technical program of PATMOS 2006 contained state-of-the-art technical contributions, three invited talks, a special session on hearing-aid design, and an embedded tutorial. The technical program focused on timing, performance and power consumption, as well as architectural aspects with particular emphasis on modeling, design, characterization, analysis and optimization in the nanometer era. The Technical Program Committee, with the assistance of additional expert reviewers, selected the 64 papers presented at PATMOS. The papers were organized into 11 technical sessions and 3 poster sessions. As is always the case with the PATMOS workshops, full papers were required, and several reviews were received per manuscript.

2. Record Nr. UNINA9910709918803321

Autore	Horlick J
Titolo	Collaborative reference program for color and appearance : retroreflectance / / J. Horlick; J. Stevenson
Pubbl/distr/stampa	Gaithersburg, MD : , : U.S. Dept. of Commerce, National Institute of Standards and Technology, , 1978
Descrizione fisica	1 online resource
Collana	NBSIR ; ; 78-1332
Altri autori (Persone)	HorlickJ StevensonJ
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	1978. Contributed record: Metadata reviewed, not verified. Some fields updated by batch processes. Title from PDF title page.
Nota di bibliografia	Includes bibliographical references.

3. Record Nr. UNINA9910741315503321

Titolo	Mitteilungen / Schweizerische Gesellschaft für Familienforschung = Bulletin d'information / Société suisse d'études généalogiques
Pubbl/distr/stampa	Schweizerische Gesellschaft für Familienforschung
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Periodico