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Autore	Lee Juhyoung
Titolo	Deep Reinforcement Learning Processor Design for Mobile Applications // by Juhyoung Lee, Hoi-Jun Yoo
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Altri autori (Persone)	YooHoi-Jun
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Nota di contenuto	Introduction -- Background of Deep Reinforcement Learning -- Group-Sparse Training Algorithm for Accelerating Deep Reinforcement Learning -- An Energy-Efficient Deep Reinforcement Learning Processor Design -- Low-power Autonomous Adaptation System with Deep Reinforcement Learning -- Low-power Autonomous Adaptation System with Deep Reinforcement Learning -- Exponent-Computing-in-Memory for DNN Training Processor with Energy-Efficient Heterogeneous Floating-point Computing Architecture.
Sommario/riassunto	This book discusses the acceleration of deep reinforcement learning (DRL), which may be the next step in the burst success of artificial intelligence (AI). The authors address acceleration systems which enable DRL on area-limited & battery-limited mobile devices. Methods are described that enable DRL optimization at the algorithm-, architecture-, and circuit-levels of abstraction. Enables deep reinforcement learning (DRL) optimization at algorithm-, architecture-, and circuit-levels of abstraction; Includes methodologies that can

reduce the high cost of DRL; Uses analysis of computational workload characteristics of DRL in the context of acceleration.
