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Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references at the end of each chapters and index.
Nota di contenuto	Introduction -- Systematic Designs -- Wireless Power Antennas -- Wireless Power Converters -- Wireless Power Management -- Power Regulations -- Design Cases -- Contributions and Future Work.
Sommario/riassunto	This book equips readers with tools for computer architecture of high performance, low power, and high reliability memory hierarchy in computer systems based on emerging memory technologies, such as STTRAM, PCM, FBDRAM, etc. The techniques described offer advantages of high density, near-zero static power, and immunity to soft errors, which have the potential of overcoming the “memory wall.” The authors discuss memory design from various perspectives: emerging memory technologies are employed in the memory hierarchy with novel architecture modification; hybrid memory structure is introduced to leverage advantages from multiple memory technologies; an analytical model named “Moguls” is introduced to explore quantitatively the optimization design of a memory hierarchy; finally, the vulnerability of the CMPs to radiation-based soft errors is improved by replacing different levels of on-chip memory with STT-RAMs. · Provides a holistic study of using emerging memory technologies in different levels of the memory hierarchy; · Equips readers with techniques for memory design with improved performance, energy consumption, and reliability; · Includes coverage of all memory levels, ranging from cache to storage; · Explains how to choose the proper

memory technologies in different levels of the memory hierarchy. .
