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	Nota di contenuto	Evolution, Development & Evolvable Hardware -- Devices and Architectures for Evolutionary Hardware -- Representations and Algorithms -- Measurement and Fitness Function -- Overcoming Variability Through Transistor Reconfiguration: Evolvable Hardware on the PAnDA Architecture -- Functional Equivalence Checking for Evolution of Complex Digital Circuits -- Fault Tolerant Applications -- Principles and Applications of Polymorphic Circuits -- A Developmental Image Compression Technique Using Gene Regulatory Networks -- Medical Applications of Evolvable Hardware -- Metamorphic Systems: A Schema for Adaptive Autonomous Systems -- Hierarchical Networks-on-Chip Architecture for Neuromorphic Hardware -- Evolvable Robot Hardware -- Developmental Evolvable Hardware -- App. A, Evolvable Hardware Practice.
	Sommario/riassunto	This book covers the basic theory, practical details and advanced research of the implementation of evolutionary methods on physical substrates. Most of the examples are from electronic engineering

applications, including transistor-level design and system-level implementation. The authors present an overview of the successes achieved, and the book will act as a point of reference for both academic and industrial researchers.