

1. Record Nr.	UNINA9910706217403321
Titolo	Building a 21st-century infrastructure for America : state of American aviation manufacturing : hearing before the Subcommittee on Aviation of the Committee on Transportation and Infrastructure, House of Representatives, One Hundred Fifteenth Congress, first session, February 15, 2017
Pubbl/distr/stampa	Washington : , : U.S. Government Publishing Office, , 2017
Descrizione fisica	1 online resource (xiii, 70 pages)
Soggetti	Aircraft industry - Certification - United States Aeronautics, Commercial - Safety regulations - United States Aeronautics, Commercial - Government policy - United States Aeronautics - United States - Safety measures Airports - United States Aeronautics and state - United States Infrastructure (Economics) - Government policy - United States Aeronautics and state Aeronautics, Commercial - Government policy Aeronautics, Commercial - Safety regulations Aeronautics - Safety measures Aircraft industry - Certification Airports Infrastructure (Economics) - Government policy Legislative hearings. Rules. United States
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	"115-2." Print version is available for sale by the Superintendent of Documents, U.S. Government Publishing Office.
Nota di bibliografia	Includes bibliographical references.

2. Record Nr.	UNINA9910437880603321
Autore	Tang Yongjian
Titolo	Dynamic-mismatch mapping for digitally-assisted DACs // Yongjian Tang, Hans Hegt, Arthur van Roermund
Pubbl/distr/stampa	New York : , : Springer, , 2013
ISBN	1-4614-1250-1
Edizione	[1st ed. 2013.]
Descrizione fisica	1 online resource (x, 164 pages) : illustrations (some color)
Collana	Analog Circuits and Signal Processing, , 1872-082X ; ; 92
Disciplina	621.38159
Soggetti	Digital-to-analog converters
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Introduction -- Digital-to-Analog Converters -- Modeling and Analysis of Performance Limitations in CS-DACs -- Design Techniques for High-Performance Intrinsic and Smart CS-DACs -- A Novel Digital Calibration Technique: Dynamic-Mismatch Mapping (DMM) -- An On-chip Dynamic-Mismatch Sensor Based on a Zero-IF Receiver -- Design Example -- Conclusions.
Sommario/riassunto	<p>This book describes a novel digital calibration technique called dynamic-mismatch mapping (DMM) to improve the performance of digital to analog converters (DACs). Compared to other techniques, the DMM technique has the advantage of calibrating all mismatch errors without any noise penalty, which is particularly useful in order to meet the demand for high performance DACs in rapidly developing applications, such as multimedia and communication systems.</p> <ul style="list-style-type: none"> <li>• Discusses fundamental performance limitations of digital to analog converters and summarizes existing design/calibration techniques;</li> <li>• Introduces a novel digital calibration technique, called dynamic-mismatch mapping (DMM) to improve both static and dynamic performance of DACs;</li> <li>• Includes two state-of-the-art DAC design examples with in-depth discussion.</li> </ul>