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Titolo	Progress in the development of a multiphase turbulent model of the gas/particle flow in a small-caliber ammunition primer [[electronic resource] /] / John R. Schmidt and Michael J. Nusca
Pubbl/distr/stampa	Aberdeen Proving Ground, MD : , : U.S. Army Research Laboratory, , [2006]
Descrizione fisica	1 online resource (vi, 36 pages) : illustrations
Collana	ARL-TR ; ; 3860
Altri autori (Persone)	NuscaMichael J
Soggetti	Ballistics, Interior Propellants Ordnance Projectiles
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Title from title screen (viewed April 2, 2010). "August 2006."
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2. Record Nr.	UNINA9910484809703321
Titolo	Reversible Computation : 7th International Conference, RC 2015, Grenoble, France, July 16-17, 2015, Proceedings // edited by Jean Krivine, Jean-Bernard Stefani
Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2015
ISBN	3-319-20860-8
Edizione	[1st ed. 2015.]
Descrizione fisica	1 online resource (VIII, 291 p. 90 illus.)
Collana	Programming and Software Engineering, , 2945-9168 ; ; 9138
Disciplina	004
Soggetti	Logic design Computer science Quantum computers Algorithms Software engineering Logic Design Computer Science Logic and Foundations of Programming Quantum Computing Theory of Computation Software Engineering
Lingua di pubblicazione	Inglese
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Nota di contenuto	Invited Paper -- Moment Semantics for Reversible Rule-Based Systems -- Reversible machines -- A Hierarchy of Fast Reversible Turing Machines -- Real-time methods in reversible computation -- Reversible Ordered Restarting Automata -- Reversible Languages -- Garbage Collection for Reversible Functional Languages -- Reverse Code Generation for Parallel Discrete Event Simulation -- Towards a Domain-Specific Language for Reversible Assembly Sequences -- Design and verification of quantum circuits -- Reversibility in Extended Measurement-based Quantum Computation -- A Fully Fault-Tolerant Representation of Quantum Circuits -- Equational reasoning about quantum protocols -- Design of reversible circuits -- Design and Fabrication of a Microprocessor using Adiabatic CMOS and Bennett

Clocking -- Improved Algorithms for Debugging Problems on Erroneous Reversible Circuits -- Ricercar: A Language for Describing and Rewriting Reversible Circuits with Ancillae and its Permutation Semantics -- Circuit Synthesis -- Technology Mapping for Single Target Gate based Circuits using Boolean Functional Decomposition -- Towards Code Optimization for Line-aware HDL-based Synthesis of Reversible Circuits -- Synthesis of Quantum Circuits for Dedicated Physical Machine Descriptions -- Short Papers -- Power-Clock Generator Impact on the Performance of NEM-Based Adiabatic Logic Circuits -- A Cost Metric for the Design of Nearest Neighbor Quantum Circuits at the Reversible Logic Level -- Towards modelling of local reversibility -- Application of Functional Decomposition in Synthesis of Reversible Circuits.

Sommario/riassunto

This book constitutes the refereed proceedings of the 7th International Conference on Reversible Computation, RC 2015, held in Grenoble, France in July 2015. The 19 papers presented together with 1 invited talk were carefully reviewed and selected from 30 submissions. The Conference on Reversible Computation particularly includes the following topics: reversible machines, reversible languages, design and verification of quantum circuits, design of reversible circuits, and circuit synthesis.
