1.

Record Nr.	UNICAMPANIASUN0110889
Autore	Warner, Stefan
Titolo	Strumenti quantitativi per la gestione aziendale : funzioni, algebra lineare e matematica finanziaria / Stefan Warner, Steven R. Costenoble ; edizione italiana a cura di Marco Li Calzi
Pubbl/distr/stampa	X, 339 p. ; 25 cm
ISBN	88-7303-896-4
Edizione	[Milano : Apogeo, 2002]
Descrizione fisica	Trad. di Giulio Taiana, rev. di M. Li Calzi.
Altri autori (Persone)	Costenoble, Steven R.
Lingua di pubblicazione	Italiano
Formato	Materiale a stampa
Livello bibliografico	Monografia

2.

Record Nr.	UNISALENTO991001187929707536
Autore	Ramsey, Frank Plumpton
Titolo	Logique, philosophie et probabilités / Frank Ramsey ; traduction sous la direction de Pascal Engel et Mathieu Marion par Nicolas Deniau ... [et al.]
Pubbl/distr/stampa	Paris : Vrin, 2003
ISBN	2711616193
Descrizione fisica	352 p. ; 22 cm
Collana	Mathesis
Altri autori (Persone)	Engel, Pascal Marion, Mathieu
Disciplina	510
Soggetti	Matematica - Fondamenti Logica
Lingua di pubblicazione	Francese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Bibliografia: p. [339]-343

3. | | |
|---|---|
| Record Nr. | UNINA9910548282403321 |
| Autore | Adler Michael |
| Titolo | Proceedings of the 2022 ACM/SIGDA International Symposium on Field-Programmable Gate Arrays / / Michael Adler |
| Pubbl/distr/stampa | New York : , : Association for Computing Machinery, , 2022 |
| Descrizione fisica | 1 online resource (201 pages) : illustrations |
| Disciplina | 621.395 |
| Soggetti | Field programmable gate arrays
Programmable array logic |
| Lingua di pubblicazione | Inglese |
| Formato | Materiale a stampa |
| Livello bibliografico | Monografia |
| Sommario/riassunto | We are delighted to welcome you to the 2022 ACM International Symposium on Field- Programmable Gate Arrays (ACM FPGA 2022). ACM FPGA is the premiere forum for the presentation of new and exciting research on all aspects of FPGA technology, which include: Novel FPGA architectures and circuits.Advances in CAD tools for FPGAs, in areas such as technology mapping, placement, routing, and others. High-level design methodologies that permit FPGA design at higher levels of abstraction.New applications for FPGAs, particularly for energy efficient and high performance computation. While we will miss the usual in-person interactions and escape from winter cold in beautiful Monterey Bay, we have done our best to provide opportunities for direct connections. We hope you will take advantage of the tools offered for interacting with authors, attending virtual poster sessions, and chatting with other attendees. This year, the program committee received 72 papers which met submission guidelines and were reviewed. Overall, 25% of reviewed papers were accepted for presentation. This year's program is 3 days, comprising 15 full research papers (10 pages) and 3 short research papers (6 pages), as well as 4 invited workshops or tutorials and 2 invited keynotes. The keynotes have extended abstracts published in the proceedings. In addition, we have 18 submissions presented as posters that appear in these |

proceedings as an abstract. FPGA 2022 marks the third year where badges have been awarded for artifacts associated with accepted papers. Artifact evaluation is an opt-in process. Accepted papers have an evaluator assigned who works with the authors to check the artifacts. The evaluator attempts to reproduce the results. Awarding of badges follows the ACM guidelines: https://www.acm.org/publications/policies/artifact-review-and-badging-current.

Badges are published with the papers and highlighted in the program. Artifact evaluation promotes reproducibility and encourages reusable open source code from our community that meets high standards. We would like to thank this year's evaluators for their hard work and efforts in helping to make this a successful initiative. A total of 7 papers were awarded artifact badges this year.