1. Record Nr. UNISA996390284003316

Autore	Greenwood Will
Titolo	Bouleuterion, or A practical demonstration of county-judicatures [[electronic resource]] : Wherein is amply explained the judicial and ministerial authority of sheriffs and coroners. Together with the original, jurisdiction, and method of keeping all countrey-courts. By Will: Greenwood, philomath
Pubbl/distr/stampa	London, : printed for William Place at Grays-Inne Gate, and John Place at Furnivals-Inne Gate in Holborn, 1675
Edizione	[The fifth edition.]
Descrizione fisica	[16], 266; 151, [1]; [36] p
Soggetti	Justice, Administration of - England Jurisdictions - Great Britain
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	"Bouleut-erion" and "Philomath." are in Greek characters. "The judicial and ministerial office of coroners", "Sheriffs tourn, and court-leet", "Curia dominicalis", "The court of pypowders" have caption titles; with separate pagination and continuous register. Reproduction of the original in the Peterhouse Library, Cambridge University.
Sommario/riassunto	eebo-0125

2. Record Nr. UNINA9910523888403321

Autore	Krylov Gleb
Titolo	Single Flux Quantum Integrated Circuit Design / / by Gleb Krylov, Eby G. Friedman
Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2022
ISBN	3-030-76885-6
Edizione	[1st ed. 2022.]
Descrizione fisica	1 online resource (252 pages)
Collana	Engineering Series
Disciplina	621.3815 621.395
Soggetti	Electronic circuits Electronics Electronic Circuits and Systems Electronics and Microelectronics, Instrumentation
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di contenuto	Chapter 1. Introduction -- Chapter 2. Physics and devices of superconductive electronics -- Chapter 3. Superconductive circuits -- Chapter 4. Rapid single flux quantum (RSFQ) circuits -- Chapter 5. Synchronization -- Chapter 6. Superconductive IC manufacturing -- Chapter 7. EDA for superconductive electronics -- Chapter 8. Compact model of superconductor-ferromagnetic transistor -- Chapter 9. Inductive coupling noise in multilayer superconductive ICs -- Chapter 10. Sense amplifier for spin-based cryogenic memory cell -- Chapter 11. Dynamic single flux quantum majority gates -- Chapter 12. Design guidelines for ERSFQ bias networks -- Chapter 13. Partitioning RSFQ Circuits for Current Recycling -- Chapter 14. GALS clocking and shared interconnect for large scale SFQ systems -- Chapter 15. Design for testability of SFQ circuits -- Chapter 16. Conclusions.
Sommario/riassunto	High efficiency, large scale, stationary computing systems – supercomputers and data centers – are becoming increasingly important due to the movement of data storage and processing onto remote cloud servers. This book is dedicated to a technology particularly appropriate for this application – superconductive

electronics, in particular, rapid single flux quantum circuits. The primary purpose of this book is to introduce and systematize recent developments in superconductive electronics into a cohesive whole to support the further development of large scale computing systems. A brief background into the physics of superconductivity and the operation of common superconductive devices is provided, followed by an introduction into different superconductive logic families, including the logic gates, interconnect, and bias current distribution. Synchronization, fabrication, and electronic design automation methodologies are presented, reviewing both widely established concepts and techniques as well as recent approaches. Issues related to memory, synchronization, bias networks, and testability are described, and models, circuits, algorithms, and design methodologies are discussed and placed in context. The aim of this book is to provide insight and engineering intuition into the design of large scale digital superconductive circuits and systems. Reviews modern research in the field of superconductive digital electronics, including novel devices and approaches; Provides comprehensive background on pertinent topics; Describes prospective methodologies for large scale integration of superconductive circuits.