1. Record Nr. UNINA9910511335203321

Autore	Van der Loo Guillaume
Titolo	The EU-Ukraine Association Agreement and deep and comprehensive free trade area : a new legal instrument for EU integration without membership / / by Guillaume Van der Loo
Pubbl/distr/stampa	Leiden, Netherlands ; ; Boston, Massachusetts : , : Brill Nijhoff, , 2016 ©2016
ISBN	90-04-29865-7
Descrizione fisica	1 online resource (434 p.)
Collana	Studies in EU External Relations, , 1875-0451 ; ; Volume 10
Disciplina	382/.914209477
Soggetti	Free trade - European Union countries Free trade - Ukraine Electronic books. Ukraine Foreign economic relations European Union countries European Union countries Foreign economic relations Ukraine
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Based on author's thesis (doctoral - Ghent University, 2014).
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Preliminary Material -- Introduction -- 'Integration Agreements' Concluded by the EU: A Useful but Tricky Legal Concept -- The conditio sine qua non: The Obligation to Apply, Implement or Incorporate a Predetermined Selection of EU acquis -- Criteria to Ensure the Uniform Interpretation and Application of the EU Law -- Overview EU integration agreements -- Background of the EU-Ukraine AA: The PCA and ENP -- Legal and Political Hurdles towards the Signing and Conclusion of the EU-Ukraine AA -- A Legal Analysis of the EU-Ukraine AA -- The EU-Ukraine DCFTA: A "Deep" and "Comprehensive" FTA? -- The 'Traditional' Scope of the DCFTA: Trade in Goods and Flanking Measures -- The DCFTA: Market Access Conditionality and Mechanisms to Ensure the Uniform Interpretation and Application of the EU Acquis -- Horizontal DCFTA Provisions and Mechanisms -- An Assessment of the EU-Ukraine DCFTA -- Sectoral Integration Agreements in the ENP: The Energy Community Treaty and the Common Aviation Area -- Conclusion -- Bibliography -- Index.
Sommario/riassunto	In The EU-Ukraine Association Agreement and Deep and

	Comprehensive Free Trade Area , Guillaume Van der Loo provides the first comprehensive legal analysis of this complex and controversial international agreement. While key political and legal hurdles towards the signing and conclusion of this agreement are analysed, its scope and contents are scrutinised and contrasted to other international agreements concluded by the EU. Specific attention is devoted to the ambitious “deep and comprehensive free trade area” and the unique provisions related to Ukraine’s approximation to the EU acquis. In particular, this book explores to what extent the agreement can be considered a new legal instrument for ‘EU integration without membership’.

2.

Record Nr.	UNISALENTO991002566449707536
Autore	Janni, Pietro
Titolo	Miti e falsi miti : luoghi comuni, leggende, errori sui Greci e sui Romani / Pietro Janni
ISBN	9788822005557
Descrizione fisica	302 p., [8] c. di tav. : ill. ; 22 cm
Collana	Storia e civiltà ; 55
Disciplina	292 938
Soggetti	Mitologia classica
Lingua di pubblicazione	Italiano
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Contiene riferimenti bibliografici

3. Record Nr. UNINA9910143633803321

Titolo	Parallel and Distributed Processing : 15 IPDPS 2000 Workshops Cancun, Mexico, May 1–5, 2000 Proceedings / / edited by Jose Rolim
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2000
ISBN	3-540-45591-4
Edizione	[1st ed. 2000.]
Descrizione fisica	1 online resource (DCL, 667 p. 370 illus. in color.)
Collana	Lecture Notes in Computer Science, , 1611-3349 ; ; 1800
Disciplina	004.36
Soggetti	Computer systems Software engineering Computer engineering Computer networks Computer science Computer science - Mathematics Mathematics - Data processing Computer System Implementation Software Engineering Computer Engineering and Networks Theory of Computation Mathematics of Computing Computational Mathematics and Numerical Analysis
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references at the end of each chapters and index.
Nota di contenuto	3rd Workshop on Personal Computer based Networks Of Workstations (PC-NOW 2000) -- Memory Management in a combined VIA/SCI Hardware -- ATOLL, a new switched, high speed Interconnect in Comparison to Myrinet and SCI -- ClusterNet: An Object-Oriented Cluster Network -- GigaBit Performance under NT -- MPI Collective Operations over IP Multicast -- An Open Market-Based Architecture for Distributed Computing -- The MultiCluster Model to the Integrated Use of Multiple Workstation Clusters -- Parallel Information Retrieval on an

SCI-Based PC-NOW -- A PC-NOW Based Parallel Extension for a Sequential DBMS -- Workshop on Advances in Parallel and Distributed Computational Models -- The Heterogeneous Bulk Synchronous Parallel Model -- On stalling in LogP -- Parallelizability of some P-complete problems -- A New Computation of Shape Moments via Quadtree Decomposition -- The Fuzzy Philosophers -- A Java Applet to Visualize Algorithms on Reconfigurable Mesh -- A Hardware Implementation of PRAM and its Performance Evaluation -- A Non-Binary Parallel Arithmetic Architecture -- Multithreaded Parallel Computer Model with Performance Evaluation -- Workshop on Parallel and Distributed Computing in Image Processing, Video Processing, and Multimedia (PDIVM 2000) -- MAJC-5200: A High Performance Microprocessor for Multimedia Computing -- A Novel Superscalar Architecture for Fast DCT Implementation -- Computing Distance Maps Efficiently Using An Optical Bus -- Advanced Data Layout Optimization for Multimedia Applications -- Parallel Parsing of MPEG Video in a Multi-threaded Multiprocessor Environment -- Parallelization Techniques for Spatial-Temporal Occupancy Maps from Multiple Video Streams -- Heuristic Solutions for a Mapping Problem in a TV-Anytime Server Network -- RPV: A Programming Environment for Real-time ParallelVision — Specification and programming methodology— -- Parallel low-level image processing on a distributed-memory system -- Congestion-free Routing of Streaming Multimedia Content in BMIN-based Parallel Systems -- Performance of On-Chip Multiprocessors for Vision Tasks (Summary) -- Parallel Hardware-Software Architecture for computation of Discrete Wavelet Transform using the Recursive Merge Filtering algorithm -- Fifth International Workshop on High-level Parallel Programming Models and Supportive Environments HIPS 2000 -- Pipelining Wavefront Computations: Experiences and Performance -- Specification Techniques for Automatic Performance Analysis Tools -- PDRS: A Performance Data Representation System -- Clix — A Hybrid Programming Environment for Distributed Objects and Distributed Shared Memory -- Controlling Distributed Shared Memory Consistency from High Level Programming Languages -- Online Computation of Critical Paths for Multithreaded Languages -- Problem Solving Environment Infrastructure for High Performance Computer Systems -- Combining Fusion Optimizations and Piecewise Execution of Nested Data-Parallel Programs -- Declarative concurrency in Java -- Scalable Monitoring Technique for Detecting Races in Parallel Programs -- 3rd IPDPS Workshop on High Performance Data Mining -- Implementation Issues in the Design of I/O Intensive Data Mining Applications on Clusters of Workstations -- A Requirements Analysis for Parallel KDD Systems -- Parallel Data Mining on ATM-Connected PC Cluster and Optimization of its Execution Environments -- The Parallelization of a Knowledge Discovery System with Hypergraph Representation -- Parallelisation of C4.5 as a Particular Divide and Conquer Computation -- Scalable Parallel Clustering for Data Mining on Multicomputers -- Exploiting DatasetSimilarity for Distributed Mining -- Scalable Model for Extensional and Intensional Descriptions of Unclassified Data -- Parallel Data Mining of Bayesian Networks from Telecommunications Network Data -- Irregular'00 Seventh International Workshop on Solving Irregularly Structured Problems in Parallel -- Load Balancing and Continuous Quadratic Programming -- Parallel Management of Large Dynamic Shared Memory Space: A Hierarchical FEM Application -- Efficient Parallelization of Unstructured Reductions on Shared Memory Parallel Architectures -- Parallel FEM Simulation of Crack Propagation — Challenges, Status, and Perspectives -- Support for Irregular Computations in Massively Parallel PIM Arrays, Using an

Object-Based Execution Model -- Executing Communication-Intensive Irregular Programs Efficiently -- Non-Memory-Based and Real-Time Zerotree Building for Wavelet Zerotree Coding Systems -- Graph Partitioning for Dynamic, Adaptive and Multi-phase Computations -- A Multilevel Algorithm for Spectral Partitioning with Extended Eigen-Models -- An Integrated Decomposition and Partitioning Approach for Irregular Block-Structured Applications -- Ordering Unstructured Meshes for Sparse Matrix Computations on Leading Parallel Systems -- A GRASP for computing approximate solutions for the Three-Index Assignment Problem -- On Identifying Strongly Connected Components in Parallel -- A Parallel, Adaptive Refinement Scheme for Tetrahedral and Triangular Grids -- PaStiX: A Parallel Sparse Direct Solver Based on a Static Scheduling for Mixed 1D/2D Block Distributions -- Workshop on Java for Parallel and Distributed Computing -- An IP Next Generation Compliant Java™ Virtual Machine -- An Approach to Asynchronous Object-Oriented Parallel and Distributed Computing on Wide-Area Systems -- Performance Issuesfor Multi-language Java Applications -- MPJ: A Proposed Java Message Passing API and Environment for High Performance Computing -- Implementing Java consistency using a generic, multithreaded DSM runtime system -- Third Workshop on Bio-Inspired Solutions to Parallel Processing Problems (BioSP3) -- Take Advantage of the Computing Power of DNA Computers -- Agent surgery: The case for mutable agents -- Was Collective Intelligence1 before Life on Earth? -- Solving Problems on Parallel Computers by Cellular Programming -- Multiprocessor Scheduling with Support by Genetic Algorithms - based Learning Classifier System -- Viewing Scheduling Problems through Genetic and Evolutionary Algorithms -- Dynamic Load Balancing Model: Preliminary Assessment of a Biological Model for a Pseudo-Search Engine -- A Parallel Co-evolutionary Metaheuristic -- Neural Fraud Detection in Mobile Phone Operations -- Information Exchange in Multi Colony Ant Algorithms -- A Surface-Based DNA Algorithm for the Expansion of Symbolic Determinants -- Hardware Support for Simulated Annealing and Tabu Search -- Eighth International Workshop on Parallel and Distributed Real-Time Systems -- A Distributed Real Time Coordination Protocol -- A Segmented Backup Scheme for Dependable Real Time Communication in Multihop Networks -- Real-Time Coordination in Distributed Multimedia Systems -- Supporting Fault-Tolerant Real-Time Applications using the RED-Linux General Scheduling Framework -- Are COTS suitable for building distributed fault-tolerant hard real-time systems? -- Autonomous Consistency Technique in Distributed Database with Heterogeneous Requirements -- Real-time Transaction Processing Using Two-stage Validation in Broadcast Disks -- Using Logs to Increase Availability in Real-Time Main-Memory Database -- Components are from Mars -- 2 + 10 ? 1 + 50 ! -- A Framework for Embedded Real-time System Design -- Best-effort Scheduling of (m,k)-firm Real-time Streams in Multihop Networks -- Predictability and Resource Management in Distributed Multimedia Presentations -- Quality of Service Negotiation for Distributed, Dynamic Real-time Systems -- An Open Framework for Real-Time Scheduling Simulation -- 5th International Workshop on Embedded/Distributed HPC Systems and Applications (EHPC 2000) -- A Probabilistic Power Prediction Tool for the Xilinx 4000-Series FPGA -- Application Challenges: System Health Management for Complex Systems -- Accommodating QoS Prediction in an Adaptive Resource Management Framework -- Network Load Monitoring in Distributed Systems -- A Novel Specification and Design Methodology Of Embedded Multiprocessor Signal Processing Systems Using High-Performance Middleware -- Auto Source Code

Generation and Run-Time Infrastructure and Environment for High Performance, Distributed Computing Systems -- Developing an Open Architecture for Performance Data Mining -- A 90k gate “CLB” for Parallel Distributed Computing -- Power-Aware Replication of Data Structures in Distributed Embedded Real-Time Systems -- Comparison of MPI Implementations on a Shared Memory Machine -- A Genetic Algorithm Approach to Scheduling Communications for a Class of Parallel Space-Time Adaptive Processing Algorithms -- Reconfigurable Parallel Sorting and Load Balancing on a Beowulf Cluster: HeteroSort -- 7th Reconfigurable Architectures Workshop (RAW 2000) -- Run-Time Reconfiguration at Xilinx (invited talk) -- JRoute: A Run-Time Routing API for FPGA Hardware -- A Reconfigurable Content Addressable Memory -- ATLANTIS — A Hybrid FPGA/RISC Based Re-configurable System -- The Cellular Processor Architecture CEPRA-1X and itsConfiguration by CDL -- Loop Pipelining and Optimization for Run Time Reconfiguration -- Compiling Process Algebraic Descriptions into Reconfigurable Logic -- Behavioral Partitioning with Synthesis for Multi-FPGA Architectures under Interconnect, Area, and Latency Constraints -- Module Allocation for Dynamically Reconfigurable Systems -- Augmenting Modern Superscalar Architectures with Configurable Extended Instructions -- Complexity Bounds for Lookup Table Implementation of Factored Forms in FPGA Technology Mapping -- Optimization of Motion Estimator for Run-Time-Reconfiguration Implementation -- Constan t-Time Hough Transform On A 3D Reconfigurable Mesh Using Fewer Processors -- Fifth International Workshop on Formal Methods for Parallel Programming: Theory and Applications FMPPTA 2000 -- A Method for Automatic Cryptographic Protocol Verification -- Verification Methods .

Sommario/riassunto

This volume contains the proceedings from the workshops held in conjunction with the IEEE International Parallel and Distributed Processing Symposium, IPDPS 2000, on 1-5 May 2000 in Cancun, Mexico. The workshopsprovidea forum for bringing together researchers,practiti- ers, and designers from various backgrounds to discuss the state of the art in parallelism.Theyfocusondi erentaspectsofparallelism,fromruntimesystems to formal methods, from optics to irregular problems, from biology to networks of personal computers, from embedded systems to programming environments; the following workshops are represented in this volume: { Workshop on Personal Computer Based Networks of Workstations { Workshop on Advances in Parallel and Distributed Computational Models { Workshop on Par. and Dist. Comp. in Image, Video, and Multimedia { Workshop on High-Level Parallel Prog. Models and Supportive Env. { Workshop on High Performance Data Mining { Workshop on Solving Irregularly Structured Problems in Parallel { Workshop on Java for Parallel and Distributed Computing { WorkshoponBiologicallyInspiredSolutionsto ParallelProcessingProblems { Workshop on Parallel and Distributed Real-Time Systems { Workshop on Embedded HPC Systems and Applications { Recon gurable Architectures Workshop { Workshop on Formal Methods for Parallel Programming { Workshop on Optics and Computer Science { Workshop on Run-Time Systems for Parallel Programming { Workshop on Fault-Tolerant Parallel and Distributed Systems All papers published in the workshops proceedings were selected by the p- gram committee on the basis of referee reports. Each paper was reviewed by independent referees who judged the papers for originality, quality, and cons- tency with the themes of the workshops.