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   Sommario/riassunto   On the 16th of June the ACM SIGPLAN Workshop on Memory System
                        Performance (MSP'02) was held in Berlin, Germany. MSP'02 is a new
                        workshop that focuses on software techniques to address the memory
                        wall.Given current technology trends that forecast a rapidly growing
                        processor-memory speed imbalance, optimizing a program's memory
                        system performance has become increasingly important. Much research
                        has focused on improving the cache performance of scientific programs
                        that use arrays as their primary data structure. Unfortunately, these
                        techniques are not a complete solution for general purpose programs,
                        which manipulate more complex and dynamic data structures. While
                        recent research has begun to address this problem, much work remains
                        to be done.The focus of this workshop is on all aspects of improving
                        the memory system performance of general-purpose programs. This
                        includes novel techniques for profiling memory system behavior, new
                        analyses (both static and dynamic) for identifying and understanding
                        memory-related performance problems, and finally optimizations for
                        improving memory system performance. Software, hardware as well as
                        hybrid approaches are encouraged. In addition, studies of application
                        that describe problems and experience with memory performance
                        related issues in specific application domains are of interest. The
                        objective of the workshop is to bring together programming language

researchers in the areas of program analysis and compiler optimization, computer architects, operating system researchers, and application programmers. We hope that this will be the first in a series of workshops that will encourage collaboration across these fields while at the same time expand program optimization research towards an area of great practical importance.MSP'02 features an invited talk and presentations of ten refereed papers. The papers were selected out of fifteen submissions. Fifteen PC members and additional external reviewers prepared multiple written reviews for each submission. The recommendations of the referees determined an initial set of papers selected for acceptance. The members of the program committee were then given the option to resolve differences in opinion for the papers they refereed based on the written reviews during the program committee tele-conferenee meeting. After the resolution process, the 10 papers included in the workshop proceedings were selected for presentation and publication.