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Autore	Hojjat Hossein
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Collana	Programming and Software Engineering, , 2945-9168 ; ; 12818
Disciplina	005.1
Soggetti	Software engineering Microprogramming Computer networks Computer science Software Engineering Control Structures and Microprogramming Computer Communication Networks Theory of Computation
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di contenuto	Coordination -- Protocol Scheduling -- Automated Replication of Tuple Spaces via Static Analysis -- Incremental Refinement of Goal Models with Contracts -- Logic -- Proof Calculi for Epistemic Logics with Structured Knowledge -- Introducing Intervalar differential dynamic logic -- A Program Logic for Fresh Name Generation -- Event-Driven Temporal Logic Pattern for Control Software Requirements Specification -- Extending OCL with map and function types -- Networks -- Deadlock in packet switching networks -- Runtime Monitoring Processes Across Blockchains -- Solving Systems of Bilinear Equations for Transition Rate Reconstruction -- Parallel Computation -- Term Rewriting on GPUs -- Promise Plus: Flexible Synchronization for Parallel Computations on Arrays -- Testing -- Towards Test Case Generation for Industrial Software Systems Based on Functional Specifications -- Compressing Automatically Generated Unit Test Suites through Test

Parameterization -- Systematic Extraction of Test Cases from Object-oriented Programs.

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Sommario/riassunto

This book constitutes the thoroughly refereed post-conference proceedings of the 9th International Conference on Fundamentals of Software Engineering, FSEN 2021, held virtually and hosted by IPM in May 2021. The 12 full papers and 4 short papers presented in this volume were carefully reviewed and selected from 38 submissions. The topics of interest in FSEN span over all aspects of formal methods, especially those related to advancing the application of formal methods in the software industry and promoting their integration with practical engineering techniques. The papers are organized in topical sections on coordination, logic, networks, parallel computation, and testing.

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