

1. Record Nr.	UNINA9910502669803321
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Titolo	OpenMP: Enabling Massive Node-Level Parallelism : 17th International Workshop on OpenMP, IWOMP 2021, Bristol, UK, September 14–16, 2021, Proceedings // edited by Simon McIntosh-Smith, Bronis R. de Supinski, Jannis Klinkenberg
Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2021
ISBN	3-030-85262-8
Edizione	[1st ed. 2021.]
Descrizione fisica	1 online resource (231 pages)
Collana	Programming and Software Engineering, , 2945-9168 ; ; 12870
Disciplina	621.3916
Soggetti	Microprocessors Computer architecture Logic design Computer programming Compilers (Computer programs) Operating systems (Computers) Processor Architectures Logic Design Programming Techniques Compilers and Interpreters Operating Systems
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di contenuto	Synchronization and Data -- Improving Speculative taskloop in Hardware Transactional Memory -- Vectorized Barrier and Reduction in LLVM OpenMP Runtime -- Tasking Extensions I -- Enhancing OpenMP Tasking Model: Performance and Portability -- OpenMP Taskloop Dependences -- Applications -- Outcomes of OpenMP Hackathon: OpenMP Application Experiences with the Offloading Model (Part I) -- Outcomes of OpenMP Hackathon: OpenMP Application Experiences with the Offloading Model (Part II) -- An empirical investigation of OpenMP based implementation of Simplex Algorithm -- Task inefficiency patterns for a wave equation solver -- Case Studies -- Comparing

OpenMP Implementations With Applications Across A64FX Platforms --
A Case Study of LLVM-Based Analysis for Optimizing SIMD Code
Generation -- Heterogenous Computing and Memory -- Experience
Report: Writing A Portable GPU Runtime with OpenMP 5.1 -- FOTV: A
generic device offloading framework for OpenMP -- Beyond Explicit
Transfers: Shared and Managed Memory in OpenMP -- Tasking
Extensions II -- Communication-Aware Task Scheduling Strategy in
Hybrid MPI+OpenMP Applications -- An OpenMP Free Agent threads
implementation.

Sommario/riassunto

This book constitutes the proceedings of the 17th International Workshop on OpenMP, IWOMP 2021, held virtually in September 2021 and hosted by the High Performance Computing research group at the University of Bristol, UK. The 15 full papers presented in this volume were carefully reviewed and selected for inclusion in this book. The papers are organized in topical sections named: synchronization and data; tasking expansions; applications; case studies; and heterogenous computing and memory. Chapter 'FOTV: A Generic Device Offloading Framework for OpenMP' is available open access under a Creative Commons Attribution 4.0 International License via link.springer.com.
