1. Record Nr. UNINA9910485151403321

Titolo	Network and parallel computing : IFIP international conference, NPC 2010, Zhengzhou, China, September 13-15, 2010 : proceedings / / Chen Ding, Zhiyuan Shao, Ran Zheng (eds.)
Pubbl/distr/stampa	Berlin, : Springer, 2010
ISBN	1-280-38888-9 9786613566805 3-642-15672-X
Edizione	[1st ed. 2010.]
Descrizione fisica	1 online resource (XII, 492 p. 275 illus.)
Collana	Lecture notes in computer science, , 0302-9743 ; ; 6289 LNCS sublibrary. SL 1, Theoretical computer science and general issues
Altri autori (Persone)	DingChen <1970-> ShaoZhiyuan ZhengRan
Disciplina	005.11
Soggetti	Parallel processing (Electronic computers) Computer network architectures
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Keynote Speech -- Building a Domain-Knowledge Guided System Software Environment to Achieve High-Performance of Multi-core Processors -- Internet-Based Virtual Computing Environment -- Session 1: Parallelization and Optimization -- Vectorization for Java -- Just-in-Time Compiler Assisted Object Reclamation and Space Reuse -- Optimization of Triangular Matrix Functions in BLAS Library on Loongson2F -- Exposing Tunable Parameters in Multi-threaded Numerical Code -- LU Decomposition on Cell Broadband Engine: An Empirical Study to Exploit Heterogeneous Chip Multiprocessors -- FDTM: Block Level Data Migration Policy in Tiered Storage System -- Session 2: Parallel Algorithms -- Scale-Adaptable Recrawl Strategies for DHT-Based Distributed Web Crawling System -- Power Efficient Scheduling for Hard Real-Time Systems on a Multiprocessor Platform -- Storage Device Performance Prediction with Selective Bagging Classification and Regression Tree -- Embedding Algorithms for Bubble-Sort, Macro-star, and Transposition Graphs -- An Efficient

Simulation Algorithm for Cache of Random Replacement Policy -- DABGPM: A Double Auction Bayesian Game-Based Pricing Model in Cloud Market -- Session 3: Network -- NPA-BT: A Network Performance Aware BitTorrent Traffic Optimization Mechanism -- User Behavior Pattern Analysis and Prediction Based on Mobile Phone Sensors -- ServiceStore: A Peer-to-Peer Framework for QoS-Aware Service Composition -- Identifying Nearest Neighbor Nodes and Connectivity in Three-Dimensional Wireless Sensor Networks Using Poisson Point Field -- A Novel Trust Evaluation Model for Mobile P2P Networks -- Session 4: Parallelization and Optimization (Cluster) -- Evaluating and Optimizing I/O Virtualization in Kernel-based Virtual Machine (KVM) -- Distributed Stream Processing with DUP -- CCIndex: A Complemental Clustering Index on Distributed Ordered Tables for Multi-dimensional Range Queries -- Online Event Correlations Analysis in System Logs of Large-Scale Cluster Systems -- Differentiated Replication Strategy in Data Centers -- Efficient Pipelining Parallel Methods for Image Compositing in Sort-Last Rendering -- Session 5: GPU and Multicore -- memCUDA: Map Device Memory to Host Memory on GPGPU Platform -- Adaptive Line Size Cache for Irregular References on Cell Multicore Processor -- Software-Hardware Cooperative DRAM Bank Partitioning for Chip Multiprocessors -- Energy-Efficient Scheduling of Real-Time Periodic Tasks in Multicore Systems -- The Core Degree Based Tag Reduction on Chip Multiprocessor to Balance Energy Saving and Performance Overhead -- Session 6: Cloud and Grid Infrastructure -- Improve Throughput of Storage Cluster Interconnected with a TCP/IP Network Using Intelligent Server Grouping -- Evaluate the Performance and Scalability of Image Deployment in Virtual Data Center -- A Resource Discovery Algorithm in Mobile Grid Computing Based on IP-Paging Scheme -- JAMILA: A Usable Batch Job Management System to Coordinate Heterogeneous Clusters and Diverse Applications over Grid or Cloud Infrastructure -- User-Centric Privacy Preservation in Data-Sharing Applications -- Software Metrics Reduction for Fault-Proneness Prediction of Software Modules -- Session 7: Network on Chip -- A Methodology for Design of Unbuffered Router Microarchitecture for S-Mesh NoC -- A Worst Case Performance Model for TDM Virtual Circuit in NoCs -- Convex-Based DOR Routing for Virtualization of NoC -- MPSoC Architecture-Aware Automatic NoC Topology Design -- ERA: An Efficient Routing Algorithm for Power, Throughput and Latency in Network-on-Chips.