

1. Record Nr.	UNINA9910485005103321
Titolo	Embedded Computer Systems: Architectures, Modeling, and Simulation [[electronic resource]] : 8th International Workshop, SAMOS 2008, Samos, Greece, July 21-24, 2008, Proceedings / / edited by Mladen Berekovic, Nikitas Dimopoulos, Stephan Wong
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2008
ISBN	3-540-70550-3
Edizione	[1st ed. 2008.]
Descrizione fisica	1 online resource (XVI, 300 p.)
Collana	Theoretical Computer Science and General Issues, , 2512-2029 ; ; 5114
Disciplina	004.22
Soggetti	Computer systems Computer networks Computer science Computers Microprocessors Computer architecture Electronic digital computers—Evaluation Computer System Implementation Computer Communication Networks Theory of Computation Computer Hardware Processor Architectures System Performance and Evaluation
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Includes index.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Beachnote -- Can They Be Fixed: Some Thoughts After 40 Years in the Business -- Architecture -- On the Benefit of Caching Traffic Flow Data in the Link Buffer -- Energy-Efficient Simultaneous Thread Fetch from Different Cache Levels in a Soft Real-Time SMT Processor -- Impact of Software Bypassing on Instruction Level Parallelism and Register File Traffic -- Scalable Architecture for Prefix Preserving Anonymization of IP Addresses -- New Frontiers -- Arithmetic Design on Quantum-Dot

Cellular Automata Nanotechnology -- Preliminary Analysis of the Cell
 BE Processor Limitations for Sequence Alignment Applications --
 802.15.3 Transmitter: A Fast Design Cycle Using OFDM Framework in
 Bluespec -- SoC -- A Real-Time Programming Model for
 Heterogeneous MPSoCs -- A Multi-objective and Hierarchical
 Exploration Tool for SoC Performance Estimation -- A Novel Non-
 exclusive Dual-Mode Architecture for MPSoCs-Oriented Network on
 Chip Designs -- Energy and Performance Evaluation of an FPGA-Based
 SoC Platform with AES and PRESENT Coprocessors -- Application
 Specific -- Area Reliability Trade-Off in Improved Reed Muller Coding
 -- Efficient Reed-Solomon Iterative Decoder Using Galois Field
 Instruction Set -- ASIP-eFPGA Architecture for Multioperable GNSS
 Receivers -- Special Session: System Level Design for Heterogeneous
 Systems -- to System Level Design for Heterogeneous Systems --
 Streaming Systems in FPGAs -- Heterogeneous Design in Functional DIF
 -- Tool Integration and Interoperability Challenges of a System-Level
 Design Flow: A Case Study -- Evaluation of ASIPs Design with LISATek
 -- High Level Loop Transformations for Systematic Signal Processing
 Embedded Applications -- Memory-Centric Hardware Synthesis from
 Dataflow Models -- Special Session: Programming Multicores -- to
 Programming Multicores -- Design Issues in Parallel Array Languages
 for Shared Memory -- An Architecture and Protocol for the
 Management of Resources in Ubiquitous and Heterogeneous Systems
 Based on the SVP Model of Concurrency -- Sensors and Sensor
 Networks -- Climate and Biological Sensor Network -- Monitoring of
 Environmentally Hazardous Exhaust Emissions from Cars Using Optical
 Fibre Sensors -- Application Server for Wireless Sensor Networks --
 Embedded Software Architecture for Diagnosing Network and Node
 Failures in Wireless Sensor Networks -- System Modeling and Design
 -- Signature-Based Calibration of Analytical System-Level Performance
 Models -- System-Level Design Space Exploration of Dynamic
 Reconfigurable Architectures -- Intellectual Property Protection for
 Embedded Sensor Nodes.

Sommario/riassunto

This book constitutes the refereed proceedings of the 8th International Workshop on Systems, Architectures, Modeling, and Simulation, SAMOS 2008, held in Samos, Greece, in July 2008. The 24 revised full papers presented together with a contemplative keynote and additional papers of two special workshop sessions were carefully reviewed and selected from 62 submissions. The papers are organized in topical sections on architecture, new frontiers, SoC, application specific contributions, system level design for heterogeneous systems, programming multicores, sensors and sensor networks; and systems modeling and design.