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Autore	Procopius : Caesariensis
Titolo	1: De bellis libri 1.-4
Pubbl/distr/stampa	Lipsiae : in aedibus B.G.Teubneri, 1963
Edizione	[Ed. stereotypa, addenda et corrigenda adiecit Gerhard Wirth]
Descrizione fisica	LXXXVII, 572 p. ; 21 cm
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Pubbl/distr/stampa	Berlin ; ; New York, NY, : Springer-Verlag, c2009
ISBN	3-642-04694-0
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Collana	Lecture notes in computer science ; ; 5779
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Altri autori (Persone)	BensalemSaddeck PeledDoron A
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Soggetti	Computer software - Verification Software engineering
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Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Rule Systems for Runtime Verification: A Short Tutorial -- Verification, Testing and Statistics -- Type-Separated Bytecode – Its Construction

and Evaluation -- Runtime Verification of Safety-Progress Properties -- Monitor Circuits for LTL with Bounded and Unbounded Future -- State Joining and Splitting for the Symbolic Execution of Binaries -- The LIME Interface Specification Language and Runtime Monitoring Tool -- A Concurrency Testing Tool and Its Plug-Ins for Dynamic Analysis and Runtime Healing -- Bridging the Gap between Algebraic Specification and Object-Oriented Generic Programming -- Runtime Verification of C Memory Safety -- A Combined On-Line/Off-Line Framework for Black-Box Fault Diagnosis -- Hardware Supported Flexible Monitoring: Early Results -- DMaC: Distributed Monitoring and Checking.

Sommario/riassunto

The RV series of workshops brings together researchers from academia and industry that are interested in runtime verification. The goal of the RV workshops is to study the ability to apply lightweight formal verification during the execution of programs. This approach complements the fine use of formal methods, which often use large resources. Runtime verification methods and tools include the instrumentation of code with pieces of software that can help to test and monitor it online and detect, and sometimes prevent, potential faults. RV 2009 was held during June 26-28 in Grenoble, adjacent to CAV 2009. The program included 11 accepted papers. Two invited talks were given by Amir Pnueli, on "Compositional Approach to Monitoring Linear Temporal Logic Properties" and Sriram Rajamani on "Verification, Testing and Statistics." The program also included three tutorials. We would like to thank the members of the Program Committee and additional referees for the reviewing and participation in the discussions.
