

1. Record Nr.	UNINA9910484681703321
Titolo	Algorithms and Architectures for Parallel Processing : 16th International Conference, ICA3PP 2016, Granada, Spain, December 14-16, 2016, Proceedings // edited by Jesus Carretero, Javier Garcia-Blas, Ryan K.L. Ko, Peter Mueller, Koji Nakano
Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2016
ISBN	3-319-49583-6
Edizione	[1st ed. 2016.]
Descrizione fisica	1 online resource (XXI, 687 p. 239 illus.)
Collana	Theoretical Computer Science and General Issues, , 2512-2029 ; ; 10048
Disciplina	004.35
Soggetti	Algorithms Software engineering Computer programming Computer networks Application software Computer science Software Engineering Programming Techniques Computer Communication Networks Computer and Information Systems Applications Theory of Computation
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di contenuto	Parallel algorithms and architectures -- Fundamental theoretical approaches -- Practical experimental projects -- Commercial components and systems -- Beyond the limits of existing technologies -- Experimental efforts, innovative systems, and investigations. Weaknesses in existing parallel processing technology.
Sommario/riassunto	This book constitutes the refereed proceedings of the 16th International Conference on Algorithms and Architectures for Parallel Processing, ICA3PP 2016, held in Granada, Spain, in December 2016. The 30 full papers and 22 short papers presented were carefully

reviewed and selected from 117 submissions. They cover many dimensions of parallel algorithms and architectures, encompassing fundamental theoretical approaches, practical experimental projects, and commercial components and systems trying to push beyond the limits of existing technologies, including experimental efforts, innovative systems, and investigations that identify weaknesses in existing parallel processing technology.
