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Autore	Canonaco Alberto <1973->
Titolo	The Beilinson complex and canonical rings of irregular surfaces // Alberto Canonaco
Pubbl/distr/stampa	Providence, Rhode Island : , : American Mathematical Society, , [2006] ©2006
ISBN	1-4704-0466-4
Descrizione fisica	1 online resource (114 p.)
Collana	Memoirs of the American Mathematical Society, , 0065-9266 ; ; number 862
Disciplina	510 s 516.3/5
Soggetti	Schemes (Algebraic geometry) Projective spaces Geometry, Algebraic Commutative rings Abelian categories Electronic books.
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
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Nota di bibliografia	Includes bibliographical references (pages 95-96) and index.
Nota di contenuto	""Contents""; ""Introduction""; ""Chapter 1. Graded schemes""; ""1.1. Graded rings and modules""; ""1.2. Modules versus graded modules""; ""1.3. Graded schemes""; ""1.4. Good graded schemes""; ""1.5. Proj of a noetherian positively graded ring""; ""1.6. Graded schemes and algebraic stacks""; ""Chapter 2. Beilinson's theorem on $P(w)$ ""; ""2.1. Koszul complex and sheaves of differentials""; ""2.2. The theorem as equivalence of categories""; ""2.3. Morphisms between sheaves of differentials""; ""2.4. Uniqueness of the minimal resolution""; ""2.5. Explicit form of the minimal resolution"" ""Appendix A. Abelian categories and derived categories"" ""A.1. Quotient of an abelian category""; ""A.2. Triangulated categories""; ""A.3. Derived categories""; ""A.4. Derived functors""; ""A.5. Some related results""; ""Bibliography""; ""Index""

2. Record Nr.	UNINA9910484181603321
Titolo	Languages and Compilers for Parallel Computing : 32nd International Workshop, LCPC 2019, Atlanta, GA, USA, October 22–24, 2019, Revised Selected Papers // edited by Santosh Pande, Vivek Sarkar
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ISBN	3-030-72789-0
Edizione	[1st ed. 2021.]
Descrizione fisica	1 online resource (175 pages)
Collana	Theoretical Computer Science and General Issues, , 2512-2029 ; ; 11998
Disciplina	004.35
Soggetti	Compilers (Computer programs) Computer systems Computer programming Microprocessors Computer architecture Compilers and Interpreters Computer System Implementation Programming Techniques Processor Architectures
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Performance of Static and Dynamic Task Scheduling for Real-Time Engine Control System on Embedded Multicore Processor -- PostSLP: Cross-Region Vectorization of Fully or Partially Vectorized Code -- FLARE: Flexibly Sharing Commodity GPUs to Enforce QoS and Improve Utilization -- Foundations of consistency types for a higher-order distributed language -- Common Subexpression Convergence: A New Code Optimization for SIMT processors -- Using Performance Event Profiles to Deduce an Execution Model of MATLAB with Just-In-Time Compilation -- CLAM: Compiler Leasing of Accelerator Memory -- Abstractions for Polyhedral Topology-Aware Tasking -- SWIRL++: Evaluating Performance Models to Guide Code Transformation in Convolutional Neural Networks -- A Structured Grid Solver with

Sommario/riassunto

This book constitutes the thoroughly refereed post-conference proceedings of the 32nd International Workshop on Languages and Compilers for Parallel Computing, LCPC 2019, held in Atlanta, GA, USA, in October 2019. The 8 revised full papers and 3 revised short papers were carefully reviewed and selected from 17 submissions. The scope of the workshop includes advances in programming systems for current domains and platforms, e.g., scientific computing, batch/ streaming/ real-time data analytics, machine learning, cognitive computing, heterogeneous/ reconfigurable computing, mobile computing, cloud computing, IoT, as well as forward-looking computing domains such as analog and quantum computing.

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